



Gregg Bartlett
Vice President, Design Technology
Technology Solutions Organization
Freescale Semiconductor, Inc.

Gregg Bartlett is currently the Vice President for Design Technology at Freescale Semiconductor. As such, he has responsibility for the Design Methodology and associated design collateral and infrastructure and advancing the design capabilities of the company.

Gregg has 24 years with Freescale Semiconductor and has had many leadership roles across Process Technology and Manufacturing. He has more than a decade in Technology Partnerships and has extensive international experience including working abroad.

He currently serves on the Semiconductor Research Board and the Semiconductor Industry Association Technology Strategy Committee and has previously served on the International Sematech Board.

He is based in Austin, Texas. He has a BSChE from Kansas State University.