

Low Power Crossbar MRAM with Scalability

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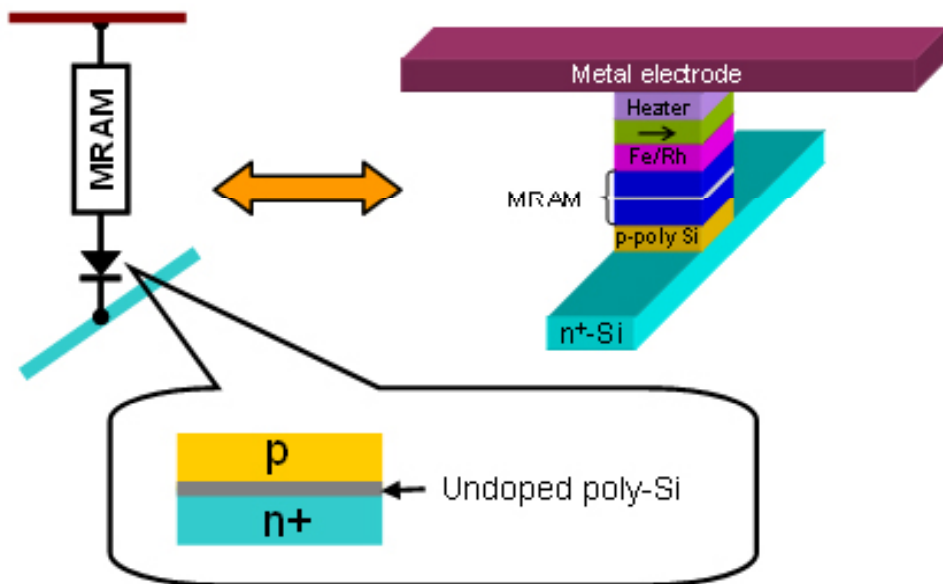
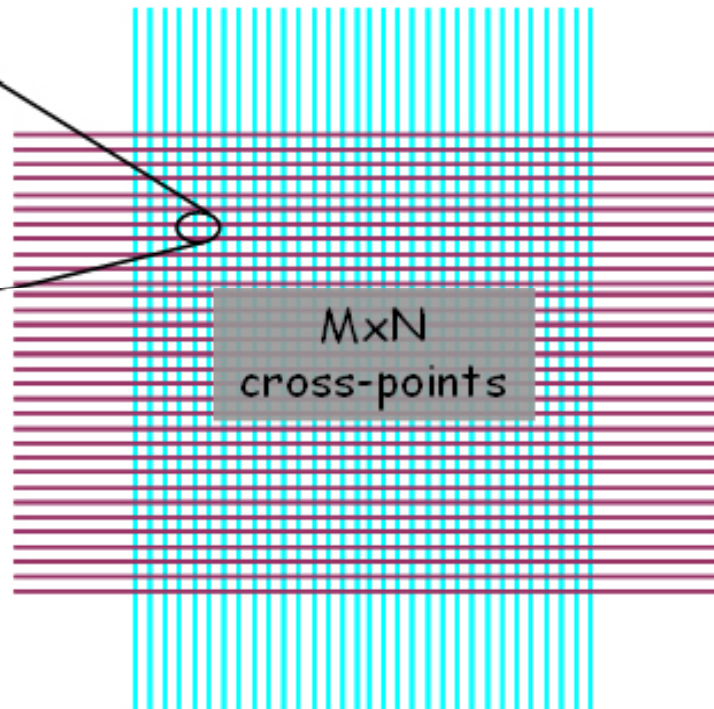
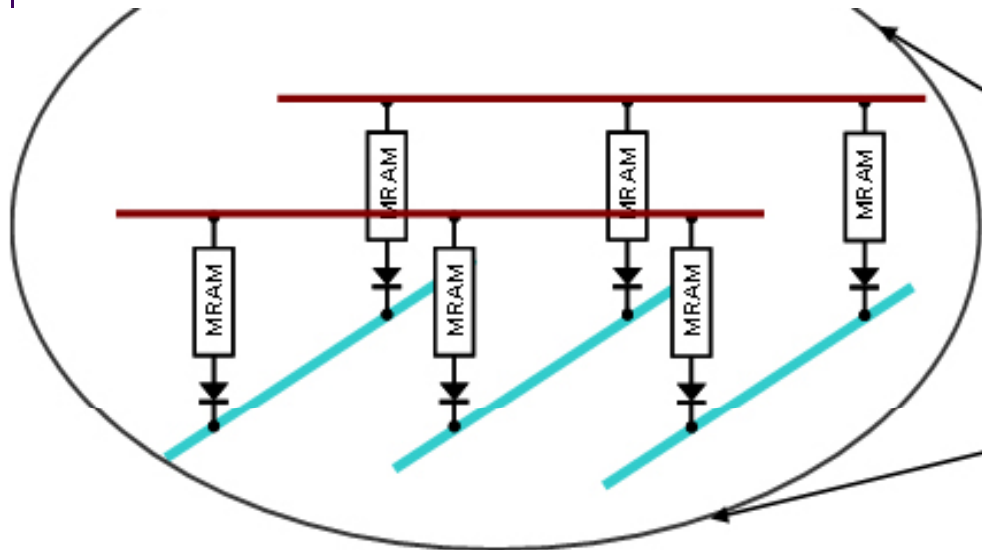
Department of Chemistry



Singapore Memory Forum

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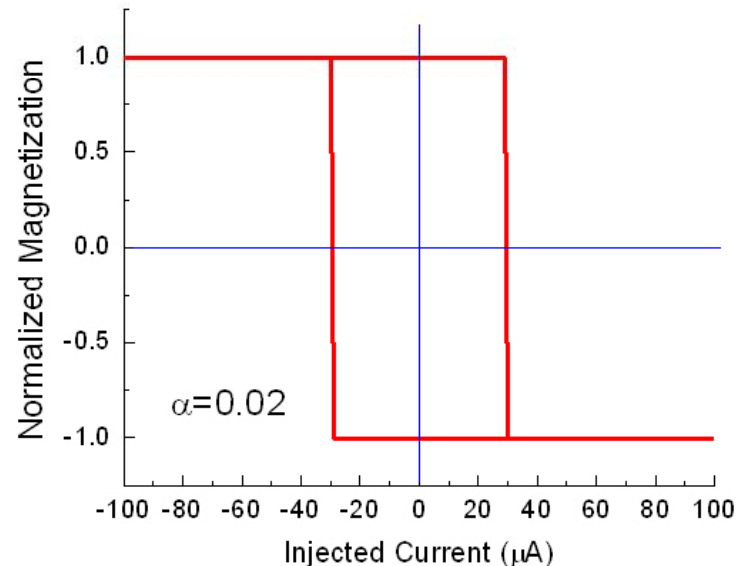
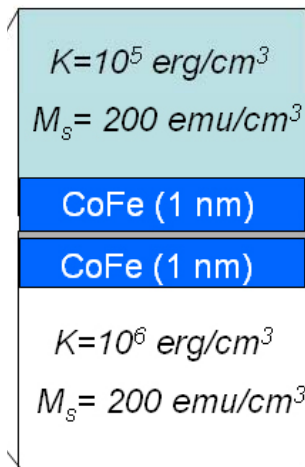
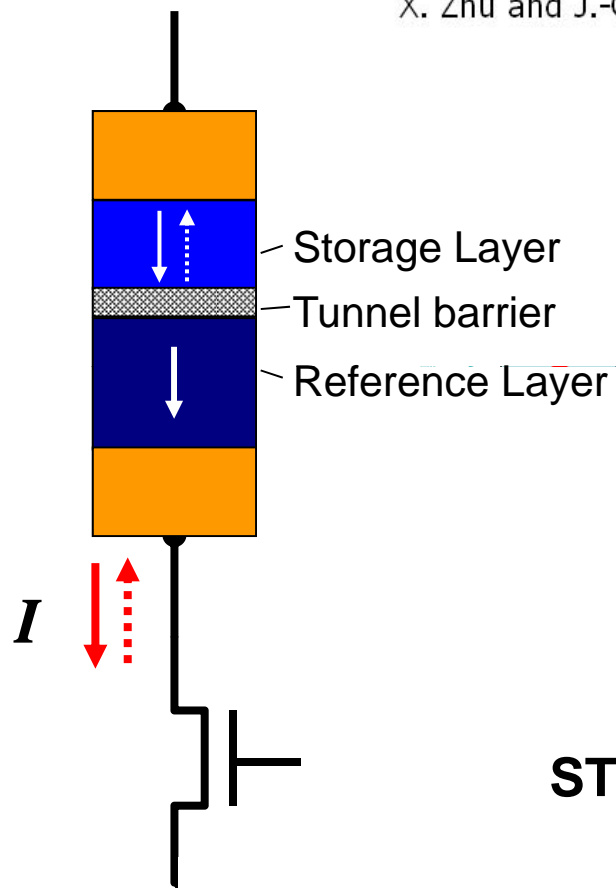
The Crossbar Design



Present STT Driven MRAM

X. Zhu and J.-G. Zhu, IEEE Trans. Magn., 42, 2739 (2006)

Element Size: 40 x 40 nm²



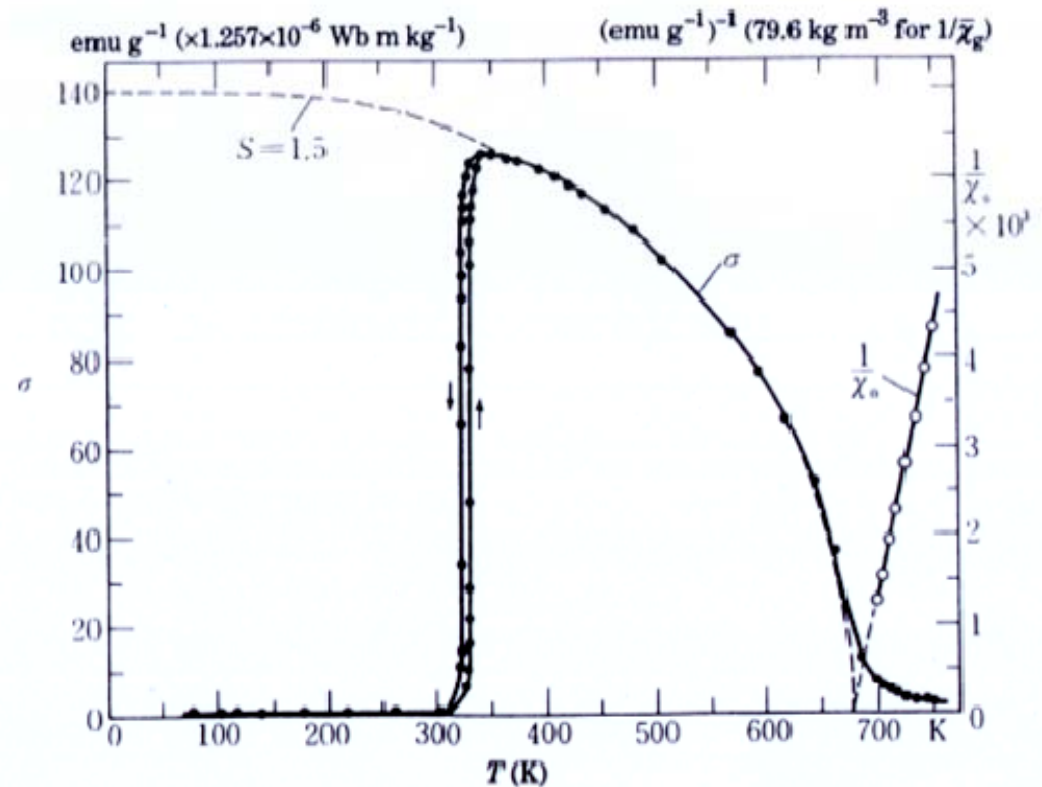
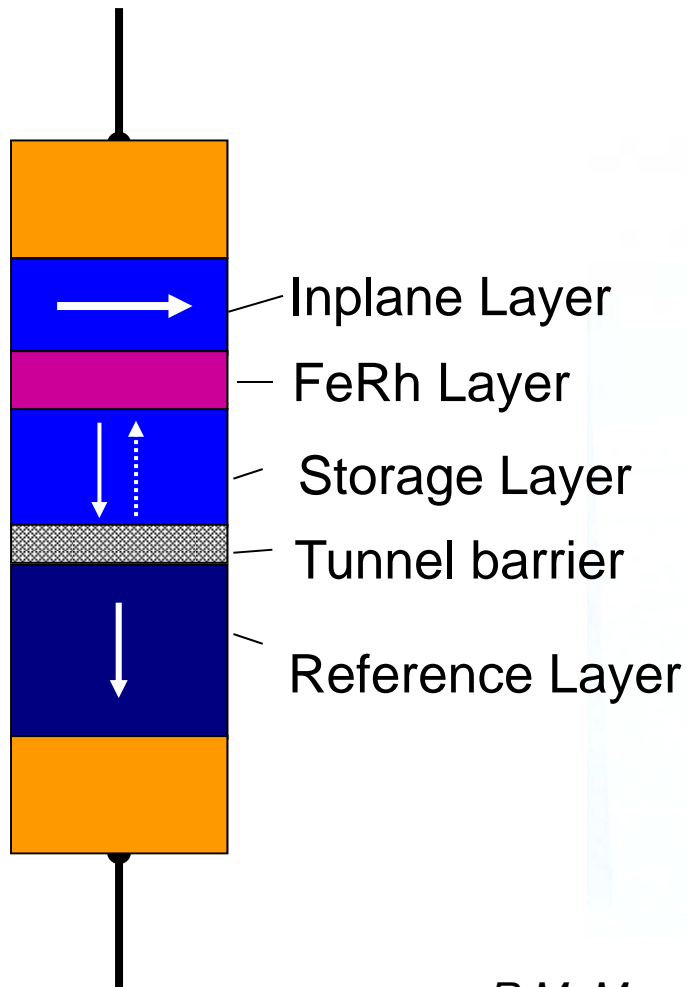
STT Switching:

$$I_{\text{switching}} = \left(\frac{2e}{\hbar} \right) \left(\frac{2\alpha}{\eta} \right) \Delta E_{\text{stored}}$$

Presently achieved: Switching energy 0.1 pJ ~ 1 pJ

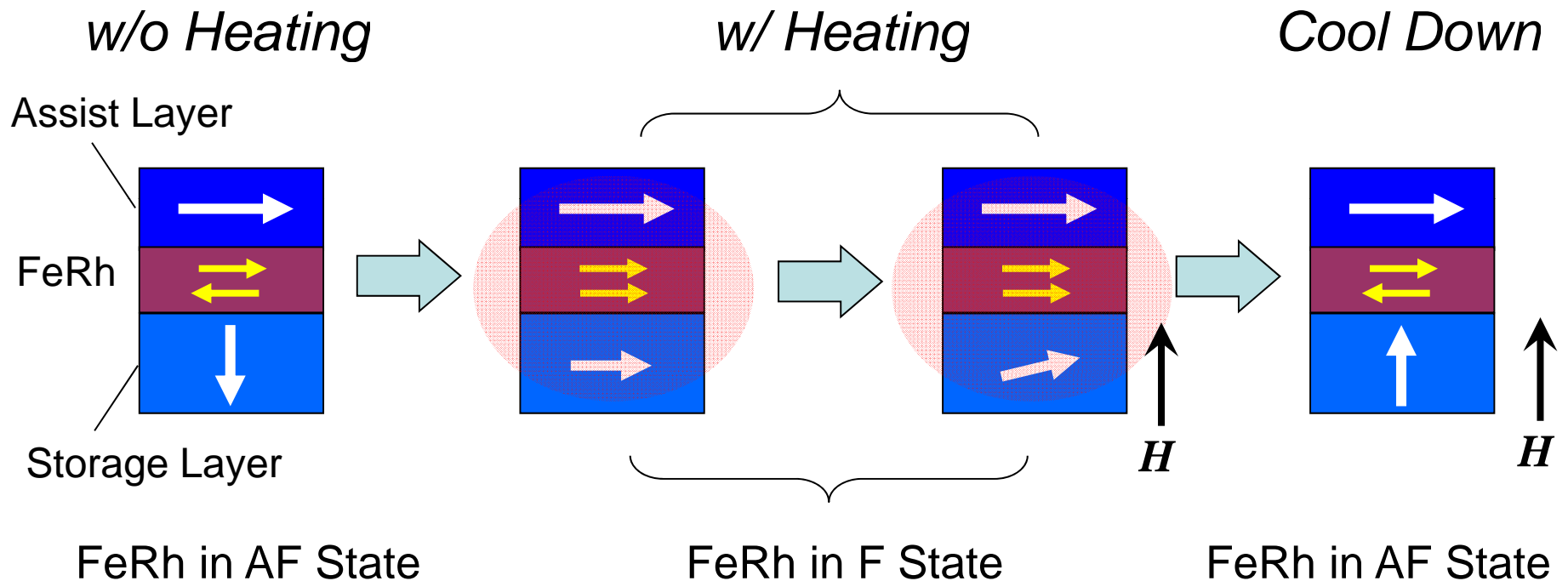
FeRh: AF to F Transition

FeRh Magnetization vs. Temperature



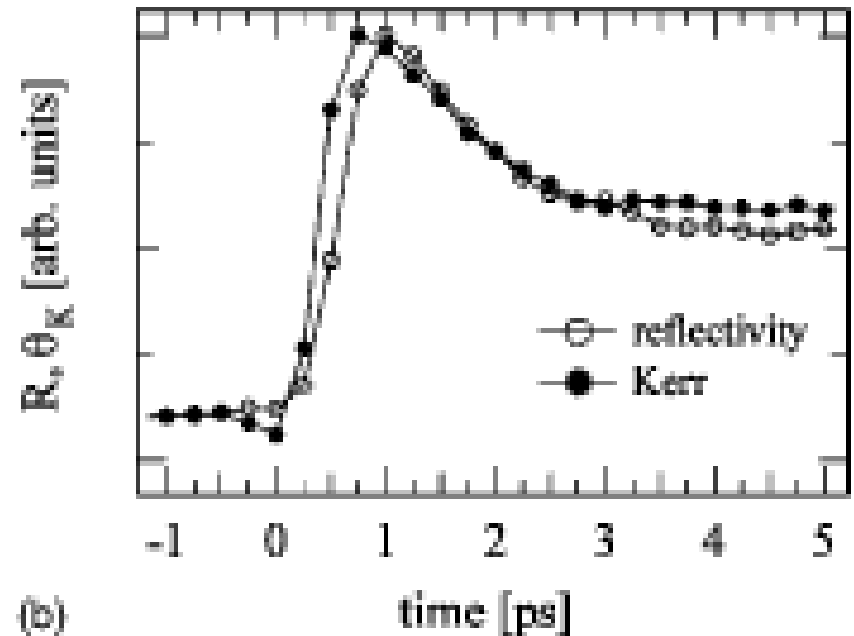
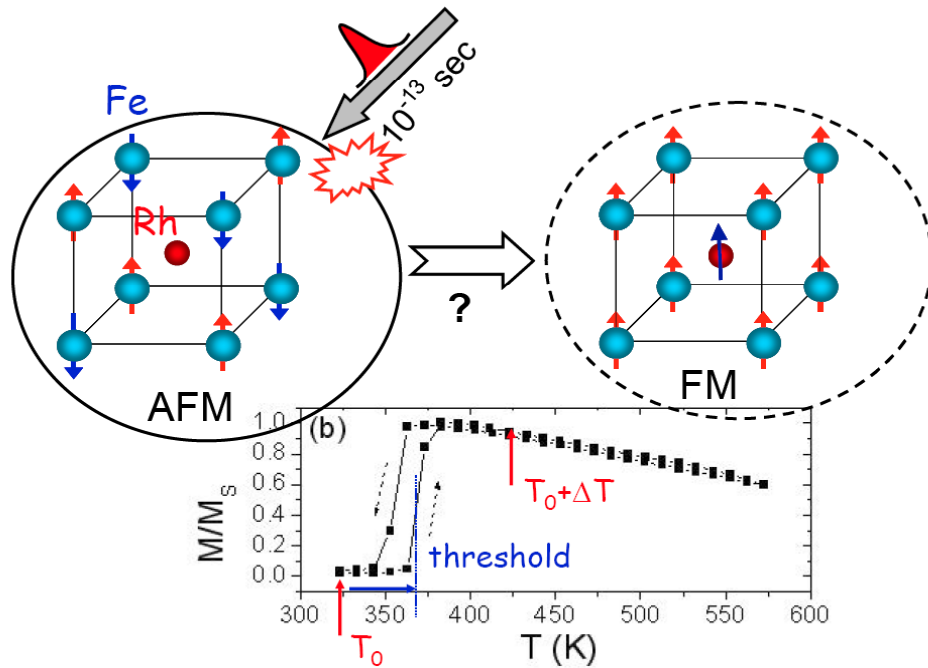
R M. Moon, W.C. Koehler, and J. Farrel, *J. Appl. Phys.* 36 (1965), 978.

S. Chikazumi, *Physics of Ferroagnetism*, Oxford Science Publications, 2nd Ed., 190.



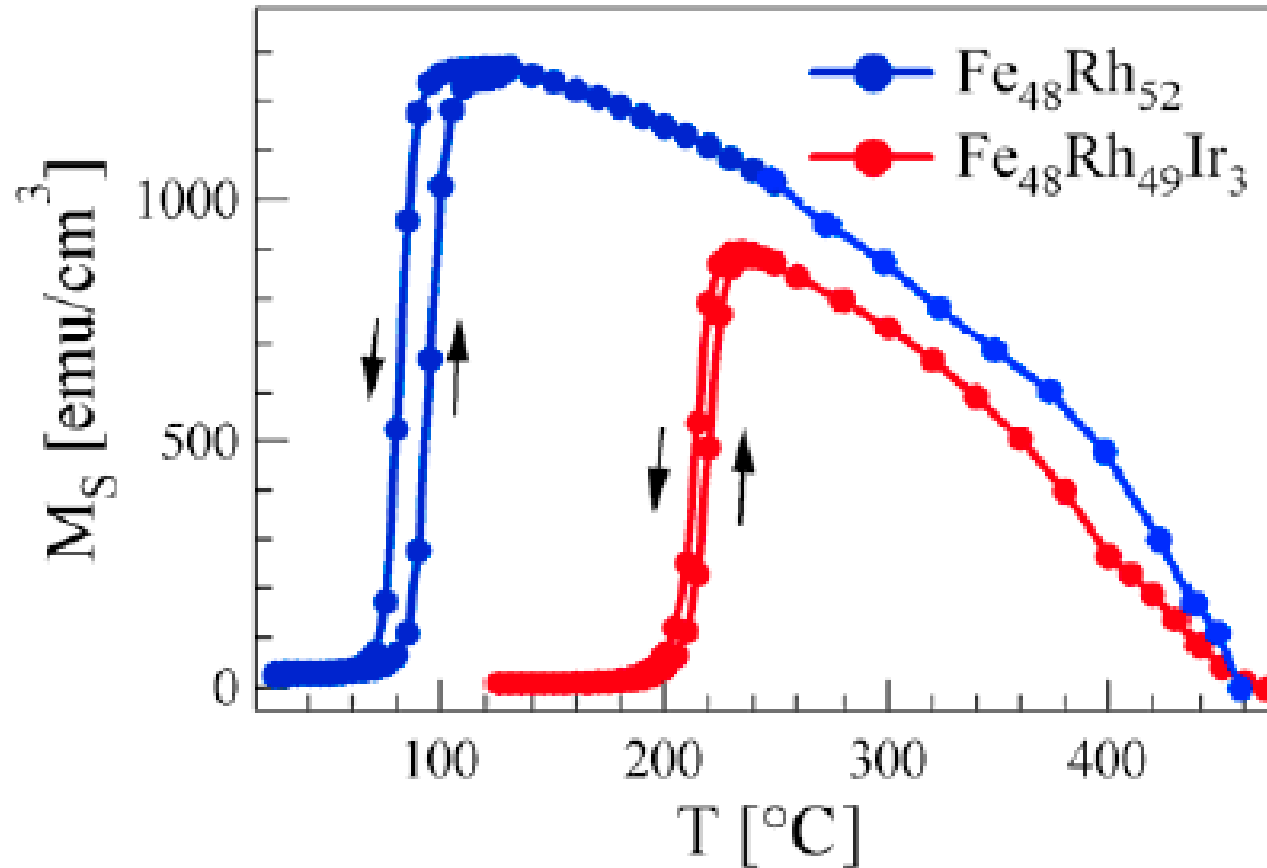
➤ ***A temperature controlled switch for magnetic coupling!***

How Fast ?



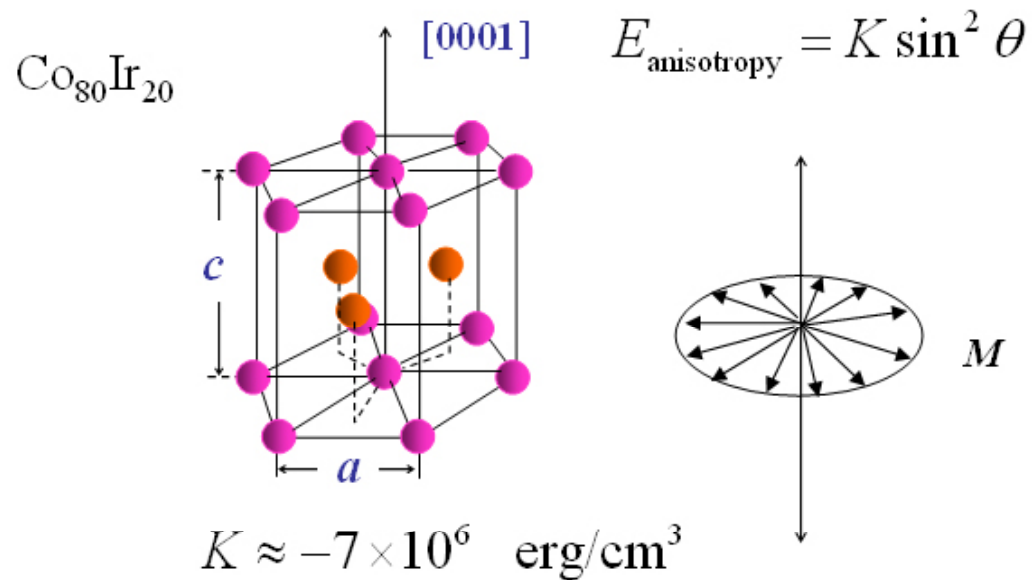
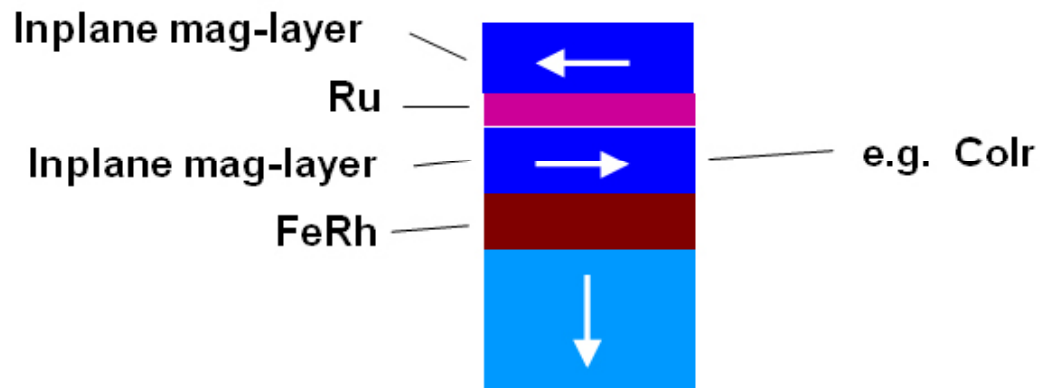
Thiele, Buess, and Back

Appl. Phys. Lett., Vol. 85, No. 14, 4 October 2004



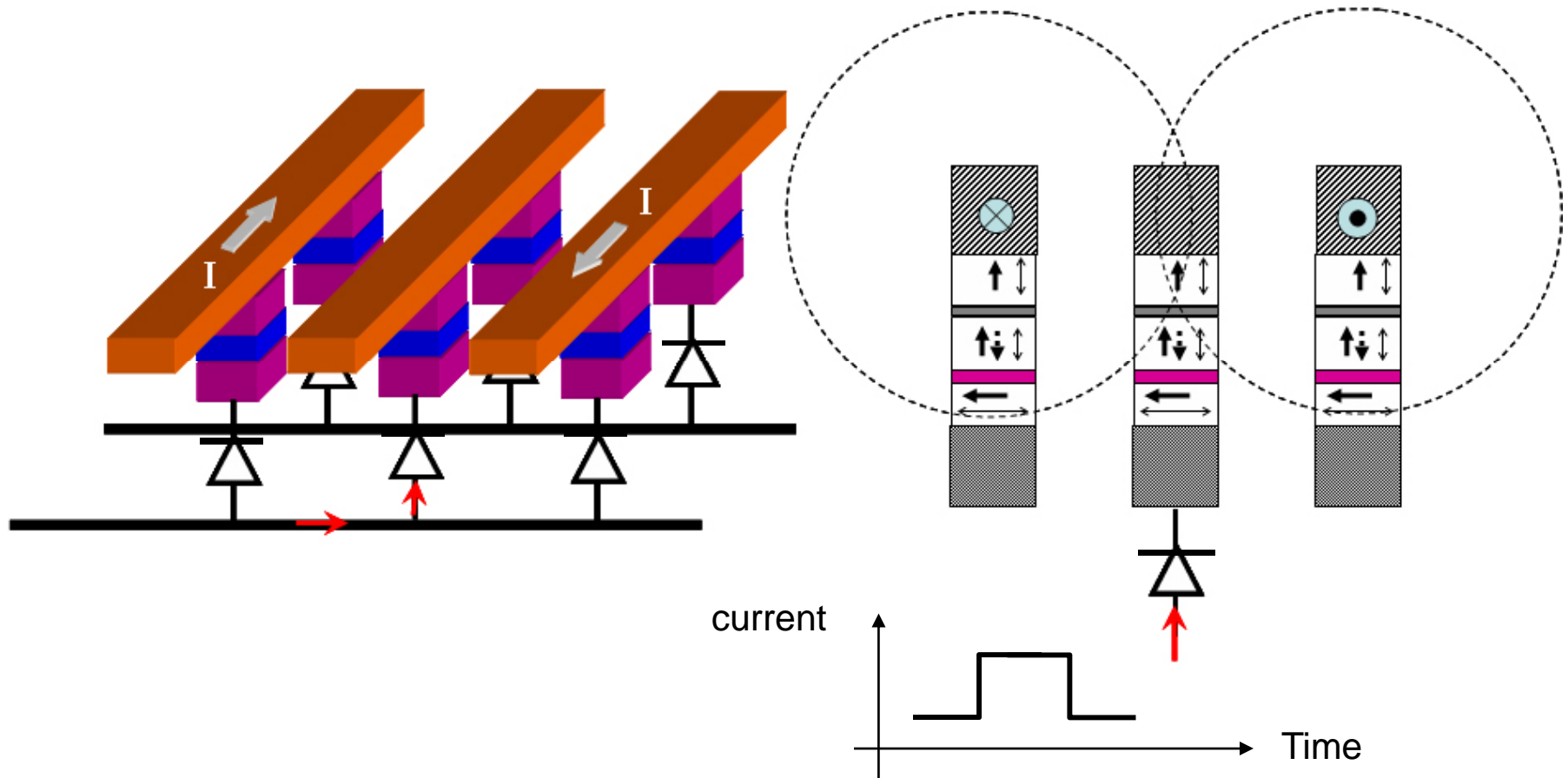
J.-U. Thiele, S. Maat, and E. E. Fullerton, *APL*, vol. 82, p. 2859 (2003)

Can Also Use Colr

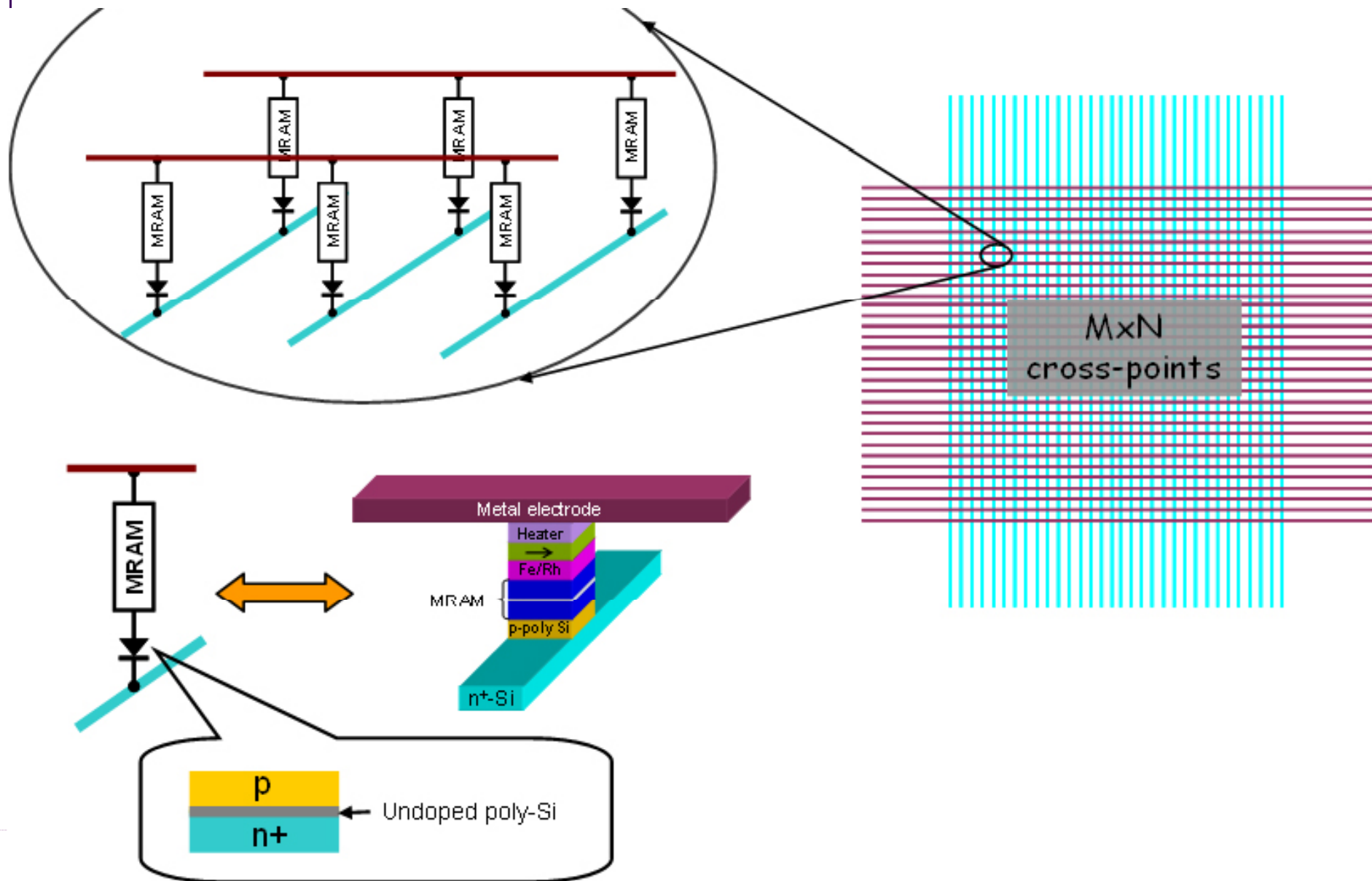


Writing Scheme

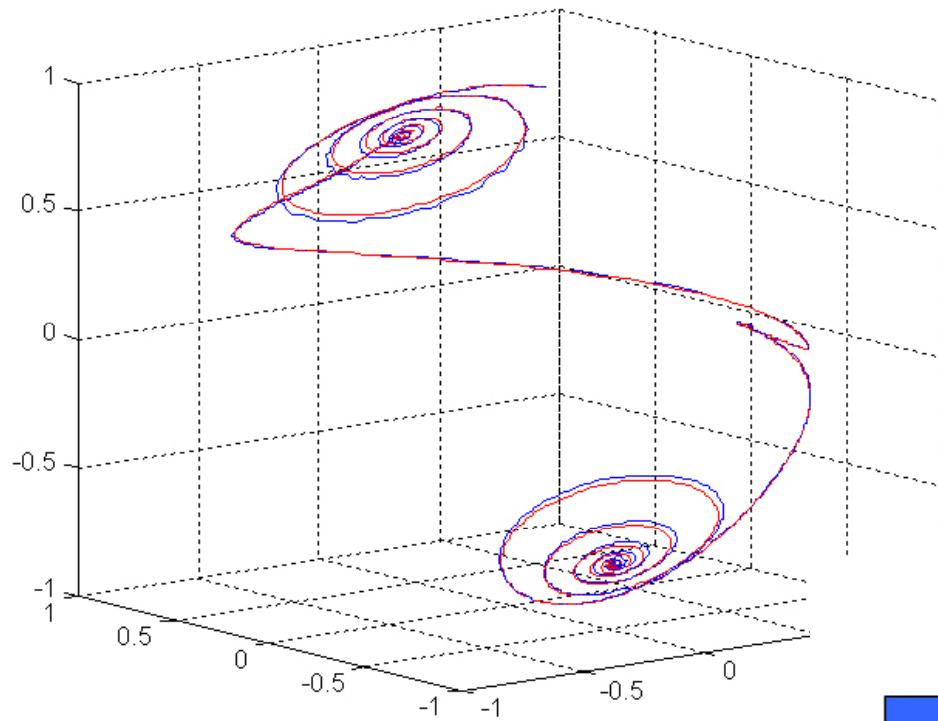
Write energy/bit: 0.01pJ



The Crossbar Design



Simulated Switching



$$H_{k, \text{ perp}} = 10^3 \text{ Oe}$$

$$H_{k, \text{ inplane}} = 5 \times 10^3 \text{ Oe}$$

$$H_{\text{ applied}} = 10 \text{ Oe}$$

With heating





Density Estimation

- ▶ True $4F^2$ memory cell
- ▶ Zero transistor per cell
- ▶ Memory element should be stable even below 10nm

At 10 nm element size:

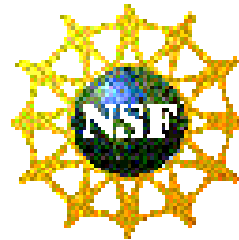
Area Density > 1.5 Tbits/in²



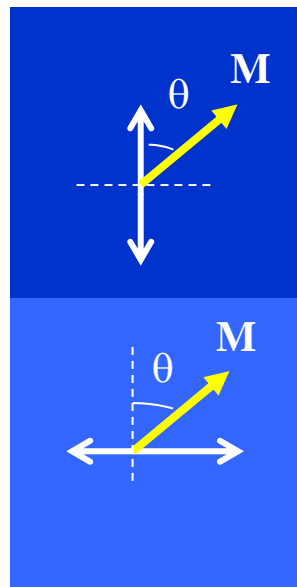
Enabling Tbits Chip Memory Device !!!



Acknowledgements



Two exchange coupled grains with orthogonally oriented easy axes:



Energy Consideration:

$$\begin{aligned}
 E &= K \sin^2 \theta + K \sin^2 (90^\circ - \theta) + 2MH \cos \theta \\
 &= K \sin^2 \theta + K \cos^2 \theta + 2MH \cos \theta \\
 &= K + 2MH \cos \theta
 \end{aligned}$$

Switching Field Threshold :

$$H_s = 0$$

Energy Barrier for State Retention:

$$\varepsilon_b = 0$$



Operation Mechanism

