



CMOx[™] Memory Technology - Applications & Products October 2009



Memory Cell Advantages

CMOXTM MEMORY TECHNOLOGY



Technology for Terabits



The 0.5F² Memory Cell



Unity achieves a 0.5F² cell size by fabricating 4 physical layers of transistor-less CMOx[™] memory in a cross-point array, and storing 2 bit/cell MLC.





Since CMOx[™] memory technology uses 4 memory layers, Unity memory products have a 4x density advantage over today's NAND Flash.



Ionic Charge Movement

Oxygen Vacancy Migration



Unity CMOx[™] memory technology works by the uniform movement of ionic charge under electric field control.



Competing Next Generation NVM Technologies: Scaling





Write & Read Data Patterns



Only Unity has achieved successful data pattern writes and reads on a passive cross-point memory array. Each data pattern represents 1024 bits.



Manufacturing Model



UNITY SEMICONDUCTOR Enterprise Storage Evolution IBM's Vision





Cost Learning Curves



On the cost learning curves, Unity's CMOx[™] achieves a cost per bit of only 1.5 times HDD bit costs, while maintaining a cost per bit advantage of 4x over NAND.







2009-2014 Product Plan



Note: "Version A" is un-shrunk product and "Version B" is a linear shrink.





Additional information located at:

http//:www.unitysemi.com

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