NRI Architecture & Device Benchmarking Workshop

Monday, August 9, 2010 • University of Notre Dame, McKenna Hall

8:00	Check-in and continental breakfast
8:30	OPENING SESSION – Chair: Alan Seabaugh, ND
	Welcome – Kerry Bernstein, <i>IBM</i> , and Wolfgang Porod, <i>ND</i>
	Introductory comments on behalf of NRI – Jeff Welser, NRI
	Introductory comments on behalf of ITRS – Ralph Cavin, SRC
9:00	Overall NRI Metric Review – Kerry Bernstein, <i>IBM</i>
	Discussion, followed by BREAK
10:00	SESSION I - Chair: George Bourianoff, Intel
	TFET, Alan Seabaugh, et al.
	TFET, Suman Datta et al.
	ExFet, Joerg Appenzeller
	BiSFET and HetTFET, Frank Register and Sanjay Banerjee
	Late Entries and Discussion, followed by BREAK
12:00	LUNCH
1:30	SESSION II - Chair: Andrew Marshall, TI
	NML, Michael Niemier and Jeff Bokor, et al.
	Spinwaves, Alex Khitun and Kang Wang
	ASL, Charles Augustine, Behtash Behin-Aein, and Supriyo Datta
	MTJ, Dejan Markovich
	Late Entries and Discussion, followed by BREAK
3:30	SESSION III - Chair: Chun-Yung Sung, IBM
	Spin FET, Peter Ye
	Thermal FET, Yong Chen
	Veselago, Ji Ung Lee
	BDD, Vijay Narayanan and Suman Datta
	Late Entries and Discussion, followed by BREAK
5:30	Industry Caucus