

## WIN 2011 – Year 1 - Phase 1.5

October 6<sup>th</sup> (all expected)

**Location:** CNSI Building 114 (Auditorium), UCLA, Los Angeles

### DAY ONE - Thursday Oct 6<sup>th</sup> 2010

<b>8:15 am</b>	<b>Light Breakfast &amp; Registration</b>
8.45 am	Welcome from NRI Sponsor (Jeff Welser)
9.00 am	WIN Overview + Plans Going Forward – Kang L Wang
9.30 am	<u>Task 1.1:</u> Imaging ultrafast dynamics of nanomagnetic logic devices and circuits, J.Bokor, R.Ramesh (Berkeley)
<b>10:00 am</b>	<b>Break</b>
10.30 am	<u>Task 1.2:</u> 1.2 Electrical detection of magnetization dynamics in nanomagnetic logic devices & circuits, S. Salahuddin, R. Ramesh (Berkeley)
11.00 am	<u>Task 2.1:</u> Low Power Spin Wave Devices and Circuits, A.Khitun, KL Wang (UCLA)
11.30 am	<u>Task 2.2:</u> Spin Wave Logic Topologies and Imaging, J.Allen, A. Kozhanov, D. Awschalom (UCSB)
<b>12.00 pm</b>	<b>Lunch + Poster Viewing</b>
2.30 pm	<u>Task 3.1:</u> Spin torque oscillators for spin wave logic, I.Krivorotov (UC Irvine) and H.Jiang (UCLA)
3.00 pm	<u>Task 3.2:</u> STT-based Circuits and Architectures, D.Markovic (UCLA)
3.30 pm	<u>Task 4.1:</u> Room Temperature SpinFET based on Dilute Magnetic Semiconductors, KL Wang (UCLA)
4.00 pm	Sponsor Caucus
6.00 pm	Reception
<b>6:30 pm</b>	<b>Dinner</b>

For any information or assistance:

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