

NMS Patterning Review Agenda

Date: Tuesday, October 4, 2011 – Thursday, October 6, 2011

Location: The Solution Center, Durham, NC 27703

Tuesday, October 4, 2011		
7:30 - 8:30 a.m.	Registration	
	Continental Breakfast	
8:00 - 8:30 a.m.	Patterning TAB Caucus (Closed Meeting - Industry Members Only)	
8:30 – 8:45 a.m.	Introductory Remarks, Welcome and Announcements	Daniel Herr SRC
8:45 - 9:15 a.m.	2129.001 - Directed Self-Assembly of Block Copolymers for Nanolithography	Caroline Ross, MIT
9:15 - 9:45 a.m.	2124.001 - Engineering of Graphene Bandgap and Growth	Michael Garner and Marjan Aslani, Stanford
9:45 - 10:15 a.m.	1864.001 - Development and Characterization of Low-Temperature Ohmic Contact between CNT and Metal Interconnects Using Focused Electron Beam Chemical Vapor Deposition	Andrei Fedorov / Songkil Kim, Georgia Tech
10:15 – 10:45 a.m.	1864.002 - Fundamentals of Formation and Nanoscale Properties of Carbon Nanotube-Metal Conductor Interfaces	Vladimir Tsukruk / Dhaval Kulkarni, Georgia Tech
10:45 - 11:00 a.m.	Break	
11:00 – 11:30 a.m.	1873.001 - Development of Graphene Based Transparent Conductive Film (TCF) for Display Applications	Isklander Kholmanov, UT-Austin
11:30 – 12:00 p.m.	2123.001 - Graphene Heterostructures for Logic and Interconnects; CVD Growth and Interface Chemistry	Jeffrey Kelber, U. of North Texas
12:00 - 12:45 p.m.	Question & Answer Session	
12:45 - 3:00 p.m.	Lunch	
	Poster Session	
3:00 - 4:00 p.m.	Member Intra-Company Caucus (Closed Meeting - Industry Members Only)	
4:00 - 6:30 p.m.	Intercompany TAB Caucus (Closed Meeting - Industry Only)	
6:30 p.m.	Dinner	

Wednesday, October 5, 2011

7:30 - 8:30 a.m.	Continental Breakfast	
8:30 – 9:30 a.m.	1841.001 - Embedded High Energy Capacity Lithium Ion Batteries Created using Inkjet Printed Nanomaterials and CMOS Packaging Techniques	Andre Taylor, Yale University
	2151.001 - Lithium Ion Micro-Batteries Assembled Using Spray and Inkjet Techniques and CMOS-Compatible Packaging	
9:30 – 10:00 a.m.	2130.001 - Atomically Precise Devices in All Three Dimensions	Michelle Simmons, U. of New South Wales
10:00 – 10:30 a.m.	2128.001 - Room Temperature Fabrication of Silicon Nanowire Arrays for Integrated Energy Storage Devices, Supercapacitors, and Sensors	Carl Vernet Thompson MIT
10:30 - 10:45 a.m.	Break	
10:45 - 11:45 a.m.	2126.001 - Multiple Domain Optimization (MDO) for Optical Lithography at 22 nm and Beyond	Bruce Smith, Rochester Institute of Technology
	2126.002 - Optical Lithography at NA Values Exceeding Media Indices	
11:45 – 12:15 p.m.	2127.001 - Hexaboride-Based Nanostructures for Heterostructure Quantum Devices in Post-Si Electronics	William Stanchina, U. of Pittsburgh
12:15 - 1:15 p.m.	Lunch	
1:15 - 2:15 p.m.	1268.001 - UW MRSEC/UW NSEC/Semiconductor Research Corporation Cooperative Program	Paul Nealey, U. of Wisconsin/Madison
	2138.001 - Experimental Determination of the Origin of Defects in Directed Self-Assembly	
2:15 – 2:45 p.m.	2138.002 - Theory and Modeling of the Origin of Defects in Directed Self-Assembly	Juan de Pablo, U. of Wisconsin/Madison
2:45 – 3:30 p.m.	Question and Answer Session	
3:30 - 3:45 p.m.	Break	
3:45 - 4:30 p.m.	Member Intra-Company Caucus (Closed Meeting - Industry Members Only)	
4:30 – 6:15 p.m.	TAB Caucus & TAB Planning Meeting (Closed Meeting - Industry Only)	
6:15 p.m.	Dinner	

Thursday, October 6, 2011

8:00 – 9:00 a.m.	Continental Breakfast	
9:00 – 9:30 a.m.	1872.001 - Demand-Pricing Relationships for Better Demand Supply Chain Planning	George Runger, Arizona State Univ.
9:30 – 10:00 a.m.	1676.001 - Device Performance Evaluation of Semiconductors with Ordered Dopant Arrays	Takashiro Shinada, Waseda University
10:00 – 10:45 a.m.	Question and Answer Session	
10:45 – 11:00 a.m.	Break	
11:00 – 12:30 p.m.	Member Intra-Company Caucus (Closed Meeting - Industry Members Only)	
12:30 – 1:30 p.m.	Lunch	
1:30 – 3:15 p.m.	TAB Caucus & TAB Planning Meeting (Closed Meeting - Industry Only)	
3:15 p.m.	Review End	