



PIONEERS IN
COLLABORATIVE
RESEARCH®

SRC/SFI/NSF Forum on Integrated Sensors for Cybersystems – FISC 2030

Session III:

Integration and Circuit Structures for Integrated
Sensor Cybersystems

March 22 – 23, 2012



Session III: Integration and Circuit Structures

1:45 – 2:15	Keynote: Simon Deleonibus LETI – France	More than Moore – Nanodevices and Microsystems Perspective
2:15	Ajith Amerasekera Texas Instruments – USA	Essential components for Sensoric Chips
	Glenn Daves Freescale – USA	Package Designs and Associated Challenges for Sensors
to	Eric Vogel Georgia Tech – USA	Circuits and Models for Silicon-based Chemical/Biological Sensors
	Manos Tentzeris Georgia Tech – USA/Ireland	RFID Tag and RF Structures on a Paper Substrate Using Inkjet-printing Technology
3:15	Naveen Verma Princeton – USA	Circuit Architectures for Inference: Sensing and Analyzing Complex Physiological Signals in Low-power Devices
3:15 – 3:45	Discussion	



Some Areas to Consider

- Nano-electronics and Micro-systems
- Building Blocks and Infrastructure
- Packaging
- Alternate Materials
- Data Processing and Communication