



PIONEERS IN
COLLABORATIVE
RESEARCH®



SRC GRC Overview

Third Annual India Design Review
January 7-8, 2015

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Welcome to the Third SRC India Design Review



- Goals:
 - Help engage India technical community
 - Expose sponsored university faculty to a wider member audience
 - Establish new ties between SRC member company professionals and supported faculty and students
 - Strengthen SRC GRC as global research partner

- January 11, 2013
 - 12 faculty, 50 industry

- January 9-10, 2014
 - 21 faculty, 56 industry

- January 7-8, 2015
 - 23 faculty, 55 industry





Semiconductor Research Corporation

- 1982: established as a consortium of US semiconductor companies to fund and manage university research



Erich Bloch,
NSF Director



Robert Noyce,
Intel founder



Jack Kilby,
Nobel Laureate



*Since 1982: \$800M member investment,
9000 students, 2000 faculty, 250 universities*

- 2007: Awarded National Medal of Technology “for building the world’s largest and most successful university research force to support the semiconductor industry”



Changes in 2015: New Thrust Structure

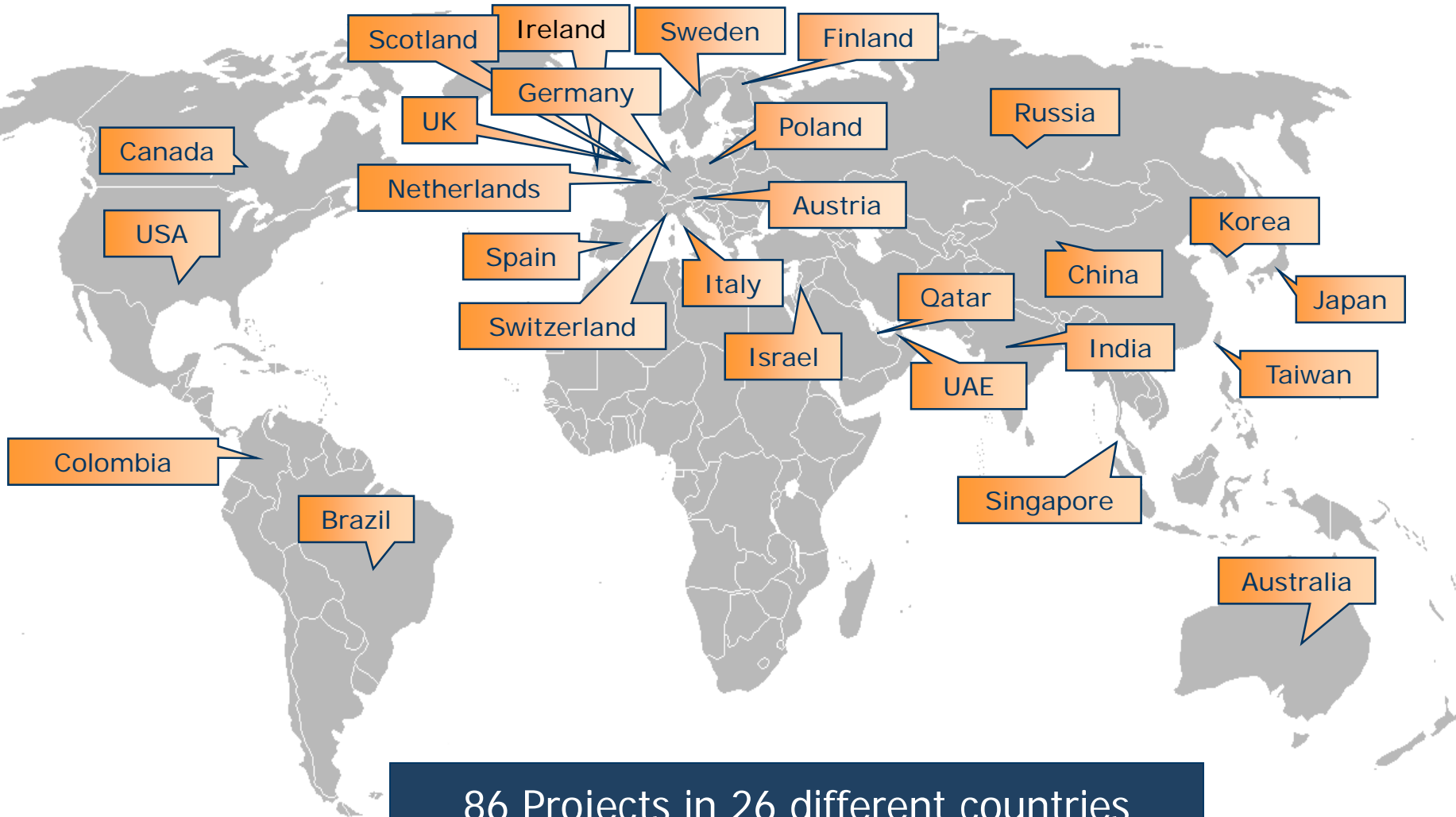


CADT	CAD and Test
SLD	System Level Design
AMS-CSD	Analog/Mixed-Signal Circuits, Systems, Devices
T3S	Trustworthy and Secure Semiconductors and Systems
I3T	Innovative and Intelligent Internet of Things
EP3C	Efficiency and Performance for Connectivity Constrained Computing
ESH	Environment, Safety & Health
PKG	Packaging
LMD	Logic and Memory Devices
NM	NanoManufacturing
SemiSynBio	Semiconductor Synthetic Biology
CSR	Cross-disciplinary Semiconductor Research

JRC[®] GRC: Global Presence

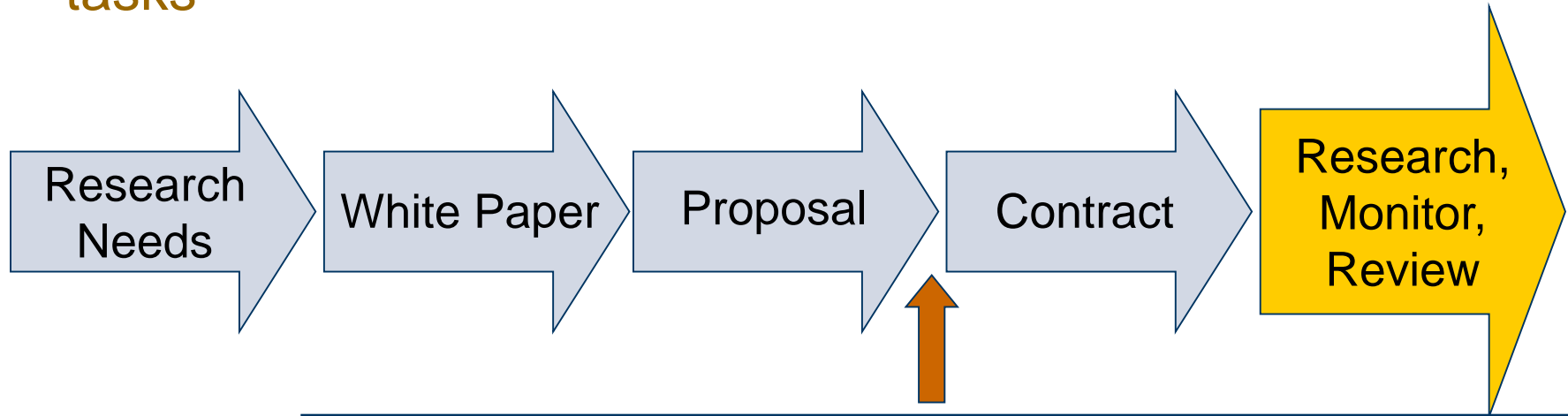


about 350 research tasks at >100 universities worldwide



86 Projects in 26 different countries
outside of USA since 2000

Core
tasks



Custom
tasks

Your company identifies projects & PIs

20% of members fees used for custom tasks



- Log on to SRC web site www.src.org
 - If you do not have a user id, click on “Register”
- Click on “Library,” then “Research Catalog”
 - Enter the research id, or the name of the researcher, or a word from the title
- Click on the task to look at the catalog page
- If it’s the right task, click on “Apply to be a Liaison” at the upper right
- You will then be sent updates of publications and reports, and can engage with the PI and students



Liaison - Levels of Involvement

or – “how much of my time is involved?”



Hardly Involved

Reasonably Involved

Most Productively Involved

- Signs up
- Looks over catalog page
- Recognizes PI's name

- Signs up
- Looks over catalog page regularly, reading deliverable reports, sharing nuggets with colleagues
- Communicates occasionally with PI
- Attends eWorkshops
- Applies ideas to his/her work

- Signs up
- Looks over catalog page regularly, reading new deliverable reports
- Interacts regularly with PI and students
- Attends eWorkshops and research reviews
- Invites PI to give seminars at company
- Co-authors papers with PI and students
- Invites students for internship
- Applies ideas to his/her company work
- Wins “Outstanding Liaison” award

Somewhere in here is the level where you begin to get value!



SRC website FAQ



David C. Yeh | Sign Out

SRC Semiconductor Research Corporation

Pioneers in Collaborative Research®

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Frequently Asked Questions

Accounts

Can you help me retrieve my username, reset my password, or register for an account?

Liaisons & Associates

What's the best way to stay current with SRC research?

How do I navigate a Science Area or Center homepage?

Personalization

What is @SRC and what can I learn from it?

How do I subscribe to SRC research and event notifications?

Search

How do I use the sitewide Google search?

How do I use the Library filters?

White Papers & Proposals

Can you help me evaluate and upload White Paper ratings or Proposal rankings?

Form & Site Errors

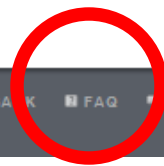
How do I fix an "illegal character" error message?

GOT A QUESTION?

Send us your questions: Questions about SRC—its programs, processes, or website.

We'll get it to the right person for the best answer. And, if it is a frequently asked question, we'll include it here.

*Topic includes video





SRC continues to increase relevance, value, and leverage to members



- Holding and recording e-Workshops, e-Kickoffs
- Providing remote access to reviews
- Providing early access to research results
- Providing access to students for internships and employment
- Engaging worldwide member design community
- Assuring member input in funding and selection process
- Focusing on applications of interest to members
- Seeking and securing leveraged funds from government and other sources

Differences from last year:

- More faculty

- About the same industry

- Mentor participation

Two tracks good – more projects of interest

- TI – analog projects

Friday visits worked out – but was helped because of

- Bangalore location

Good to have again next year

Academics – would like to follow at VLSI conf location – but

- industry participation could be limited

What about WebEx? Would like to encourage face to face –

- but would consider remote listening and/or recording

Could members talk briefly about internships?
Some faculty find it difficult to attend because of travel cost.

Need to publicize solicitations to India community.

List start-stop dates of projects on agenda.
Could we have graduating students highlighted.
Be sure to emphasize new material.

Follow up on SRC funding targeted to India and matching
funds from the government.

List of areas of interest from industry participants.
Companies make universities aware of problems of interest.
SRC make faculty aware of calls for proposals.

Good to invite faculty who are not yet funded.

Need to assess whether having in conjunction with VLSI Conf is a benefit – conflicts with some university schedules (including in India). December?

Can this be the “real” review (for projects in India)?

Benefit for all to see what others are doing

Would like to see liaison sign ups