



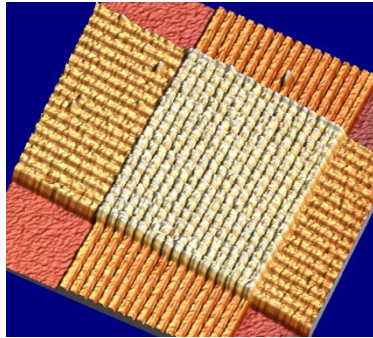
Experimental demonstration of software-trained neural network inferencing in analog memristor crossbar arrays

Miao Hu, Qiangfei Xia*, J. Joshua Yang*, R. Stanley Williams, and John Paul Strachan

Hewlett Packard Labs, Hewlett Packard Enterprise, Palo Alto CA

*UMass Amherst

DPE: Memristor arrays for computing



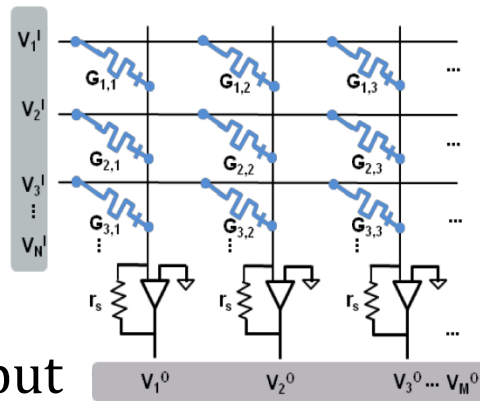
- Parallel multiply & add through Kirchoff and Ohm's law
 - 1961, K. Steinbuch "*Die Lernmatrix*" – suggests using "ferromagnetic toroids"
- Memristors as highly scalable, tunable analog resistors

High ON/OFF ratio ($\sim 10^5$), supporting multiple levels
→ HPE differentiator vs competing accelerator designs

- Advantages:
 - Well suited for streaming workloads; Key advantage is in-memory processing; Many ways to scale up
- Many Teams have been working in this field:

IBM, GeorgiaTech (Husle), UMichigan (W.Lu), ASU (S. Yu), Duke (H.Li), and many others

Input Voltage vector



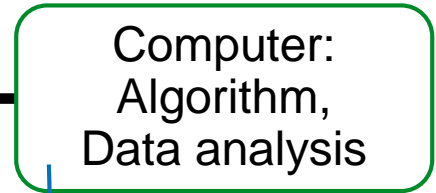
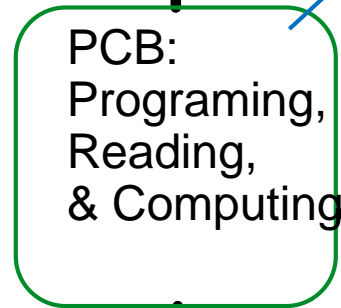
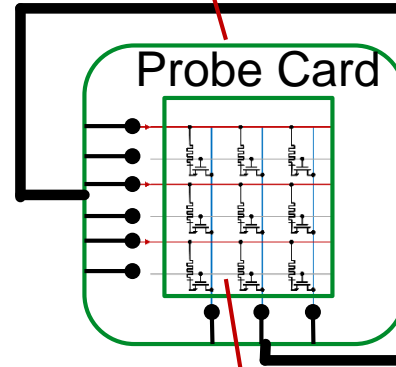
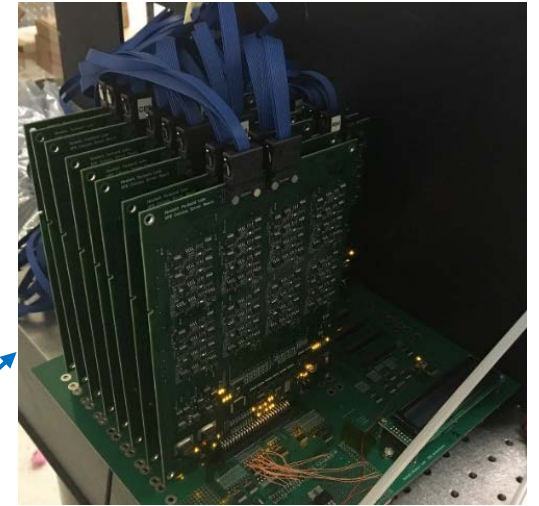
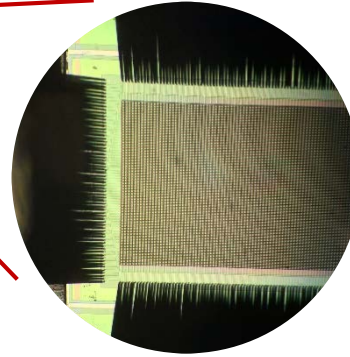
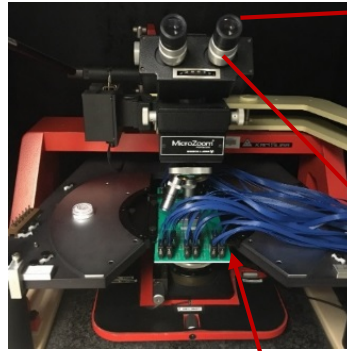
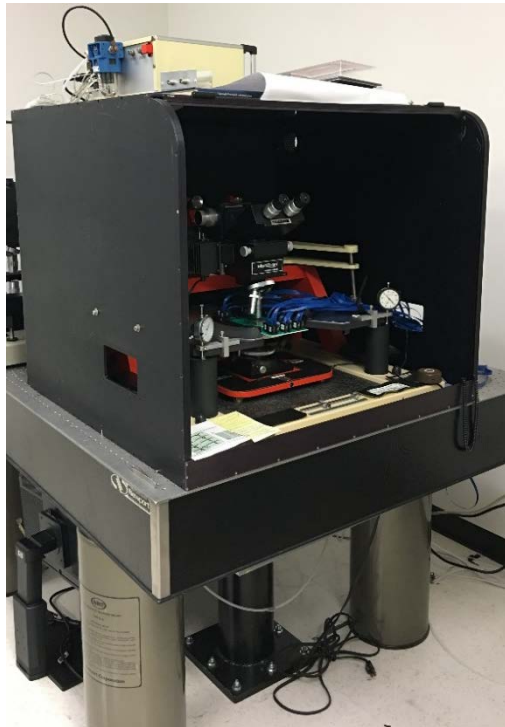
Output current

$$I_j^O = \sum_i G_{ij} \cdot V_i^I$$

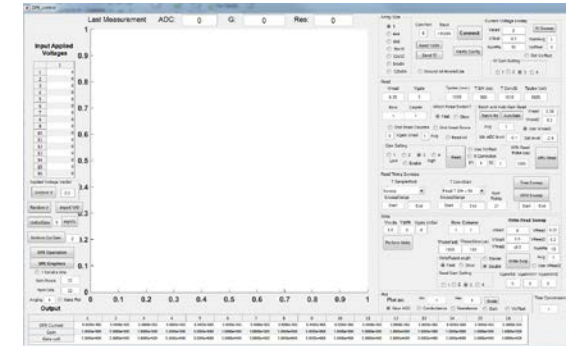
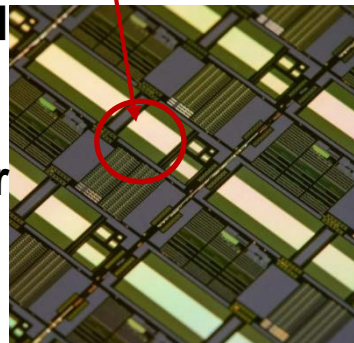
Is this true in the real world?

Dot-product Engine demonstrator

- Flexible peripheral circuit platform to study the **behavior of actual memristor crossbars** for in-memory computing.



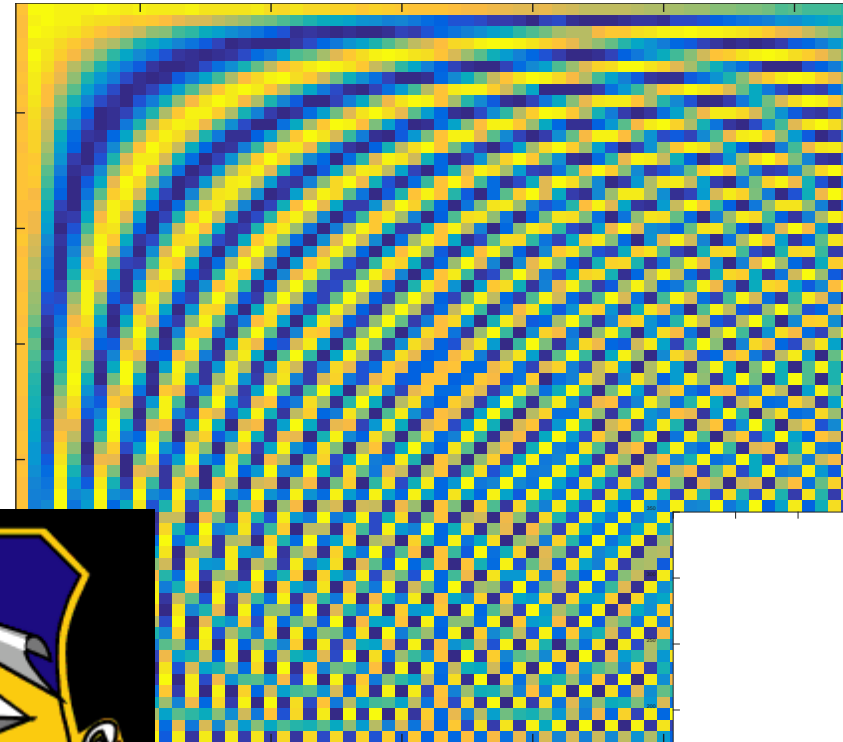
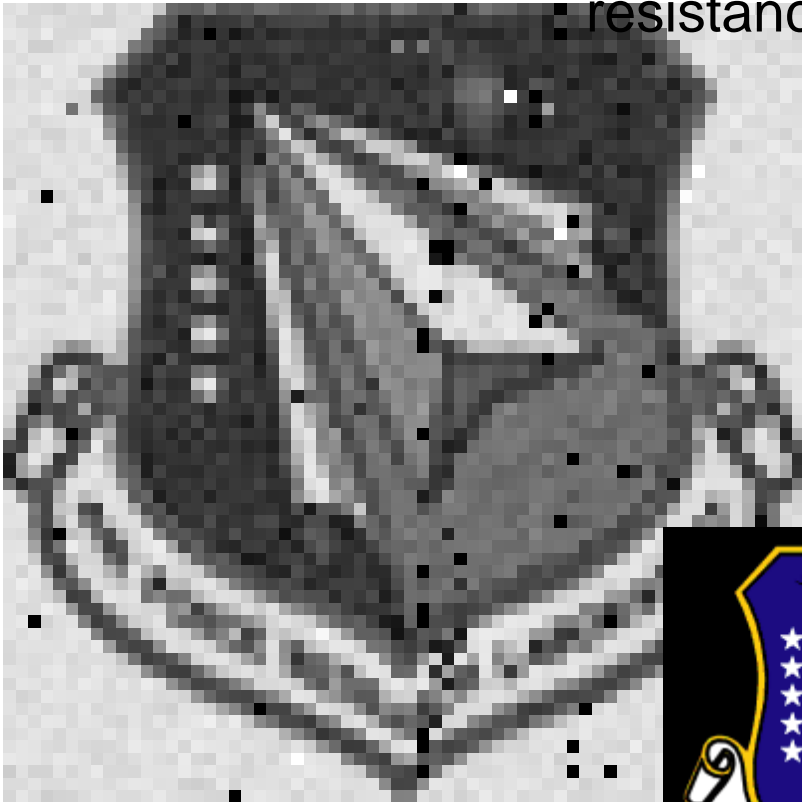
Integrated Tantalum Oxide memristors



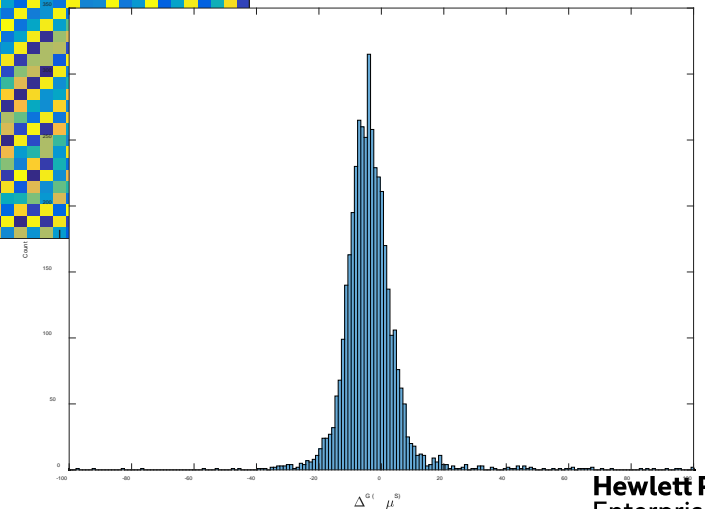
Programming full memristor arrays

64x64 = 4096 memristors (TaO_x)

~6 bits at each memristor (full range of accessible resistance)



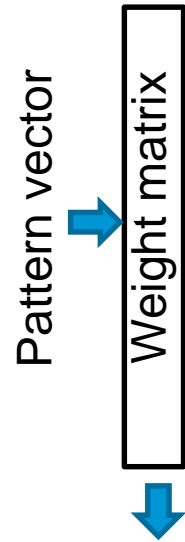
Histogram of error around zero



MNIST Pattern recognition demonstration

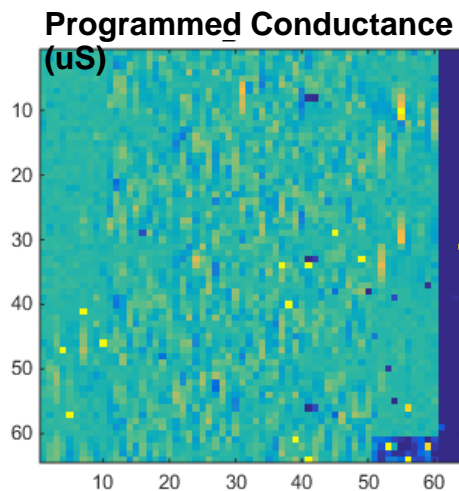
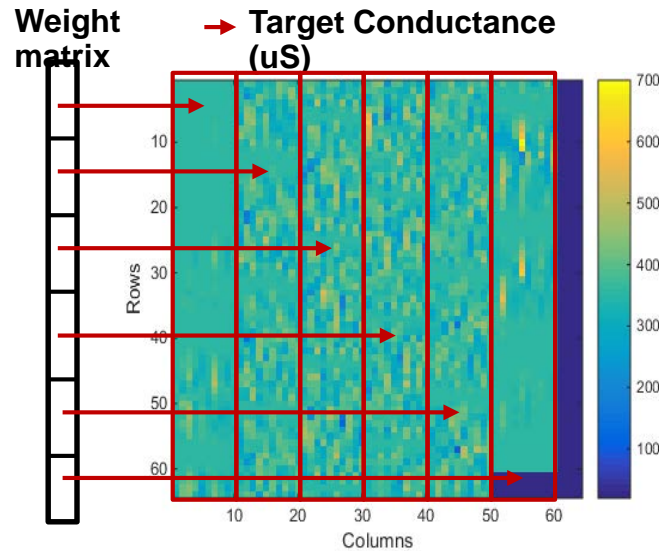
Neural network

1 layer softmax
Neural network

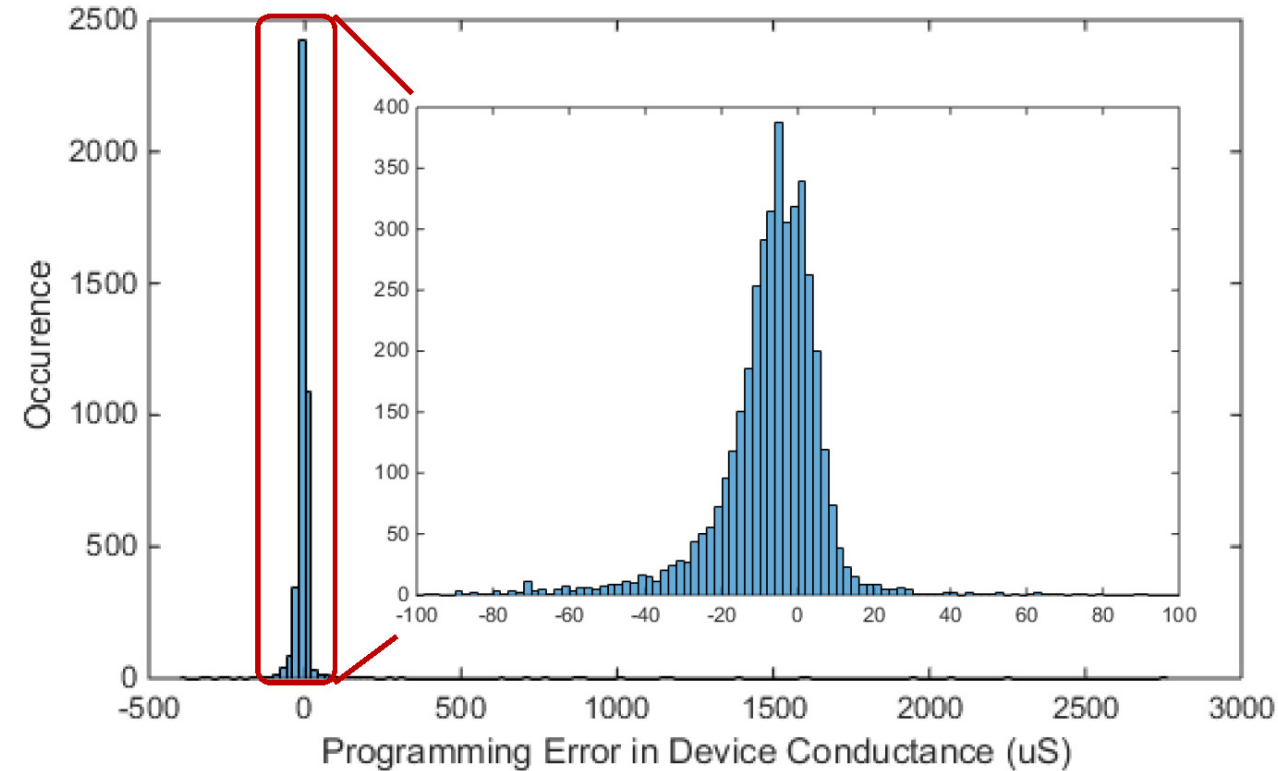


10 values, entry
with maximum
value is the
prediction

Partition and program (100 uS to 700 uS)

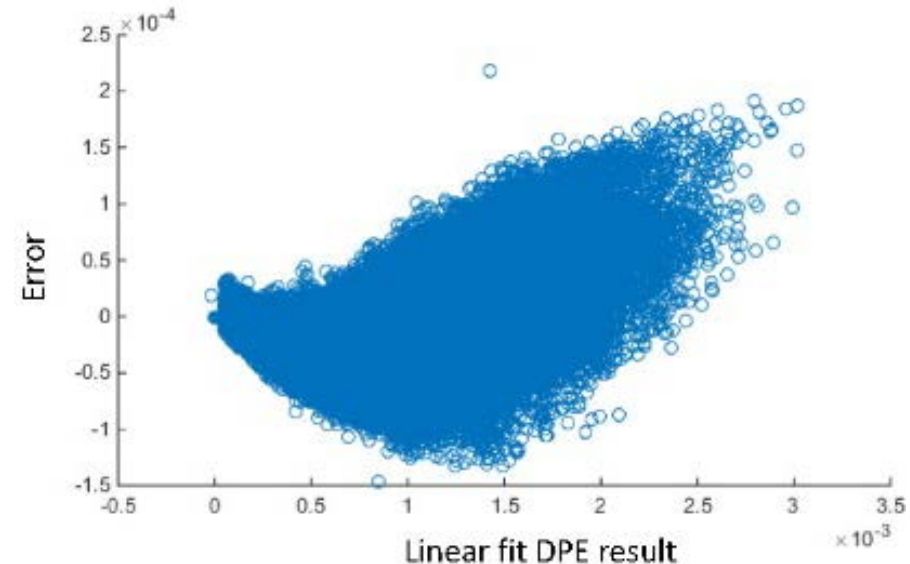
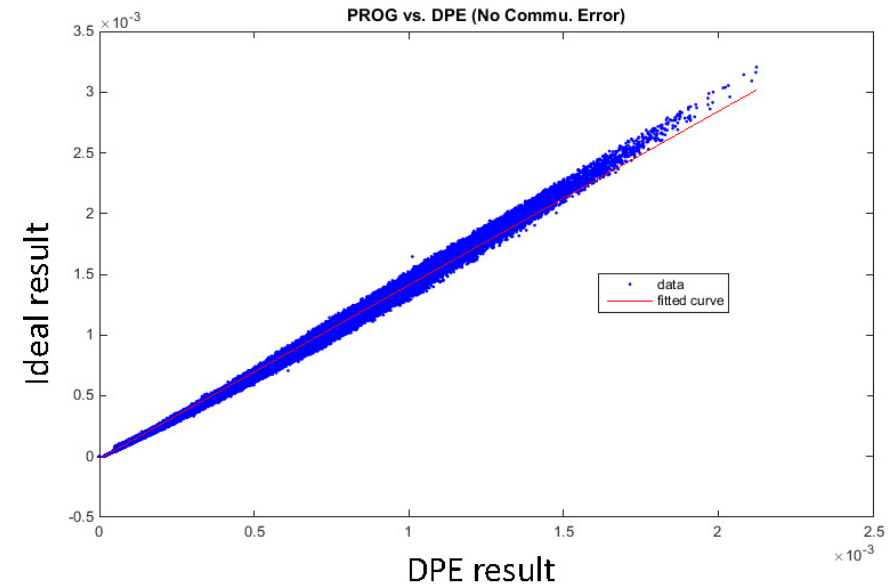


Programming error distribution



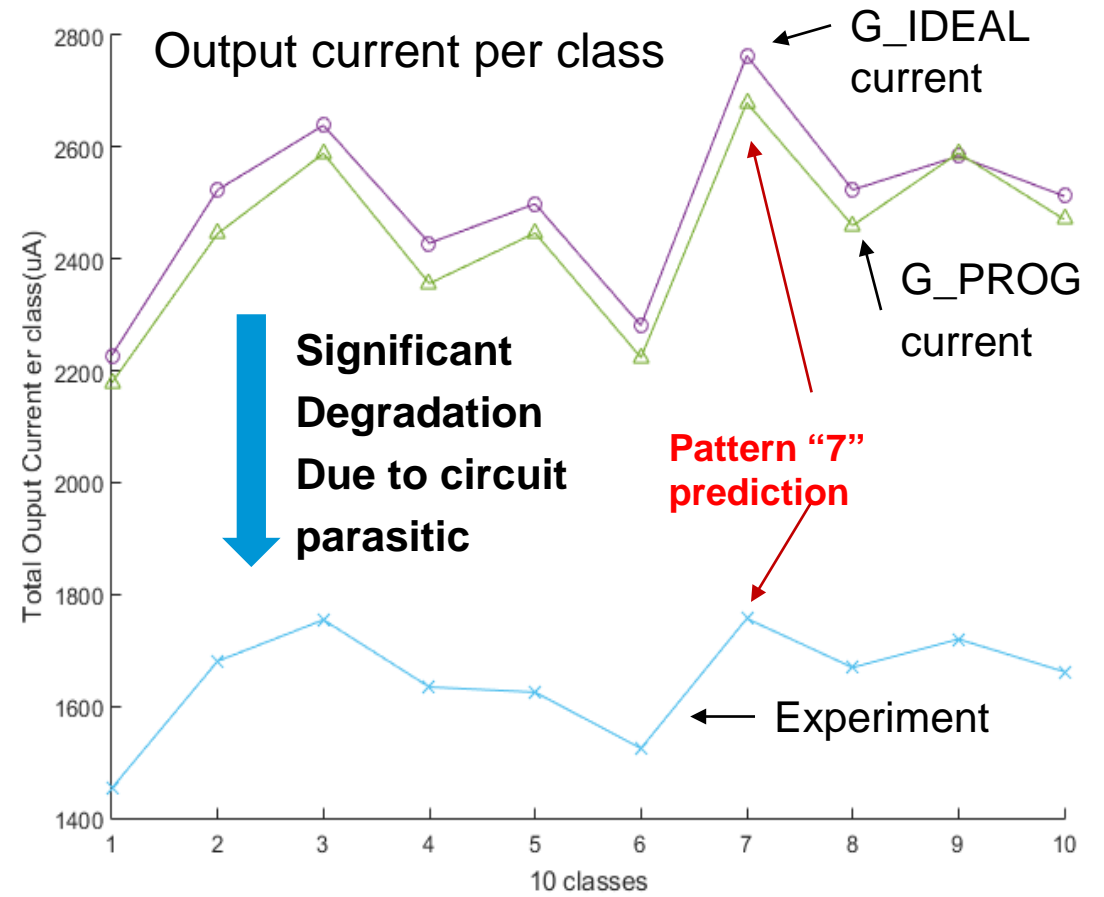
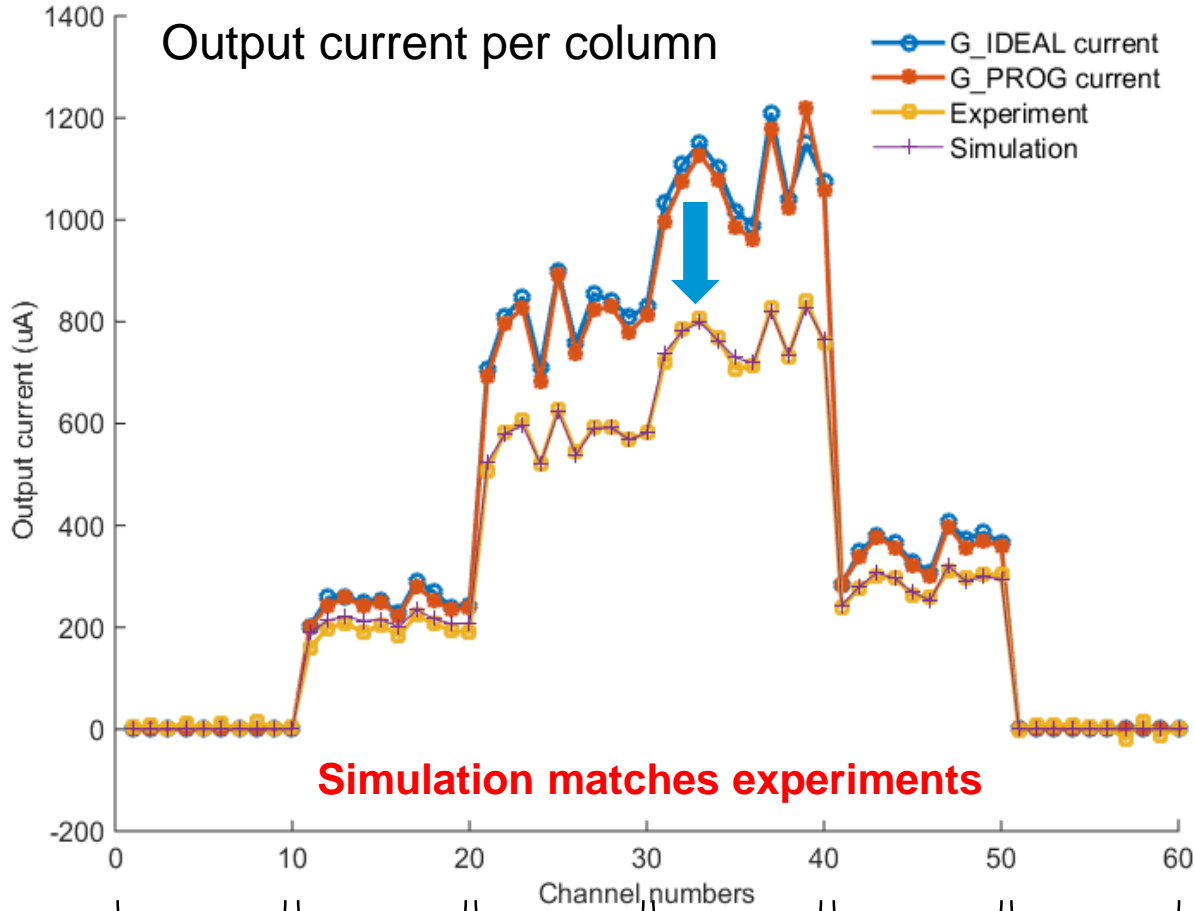
Computing accuracy of a 64x64 crossbar

- **Crossbar parameters:**
- Wire per segment ≈ 1 ohm
- Input/output resistance ≈ 1 ohm
- Device resistance: 1.4k to 10k ohm (100 uS to 700 uS)
- **Computing accuracy**
- 150k (2.5k * 60) data points.
- Memristor is <4 bit for the given range
- Output accuracy is ~ 4 bit.
- **Noise is nonlinear** due to circuit parasitics.



Pattern "7" recognition

$G_IDEAL \text{ current} = V_{in} * G_IDEAL$; $G_PROG \text{ current} = V_{in} * G_PROG$



Part1 + Part2 + Part3 + Part4 + Part5 + Part6 = Current

per class

MNIST pattern recognition accuracy

- Using a software-trained weight matrix, a single 64x64 crossbar achieves 85% accuracy (90% is ideal) for MNIST with post processing
- Single-layer NN highly sensitive to even a few defects
- Next steps:
- Better matrix to conductance mapping:
- Implement the “conversion algorithm” taking non-idealities into account
- Use Multi-layer NNs more resilient to defects:

