



Semiconductor
Research
Corporation

Because the future can't wait, we bring the best minds together to achieve the unimaginable

Nanomanufacturing Materials and Processes e-Kickoff



May 17, 2022

Virtual

Kashyap Yellai, Science Director

Tameka Bell, Research Program Coordinator

<https://www.src.org/program/grc/nmp/>
<https://www.src.org/calendar/e007645/>

SRC Select Disclosure



Thank you!

On Behalf of the SRC,

Thank You!

To all the industry members for their sponsorship and mentorship

To all the Principal Investigators & their Students

To Tameka Bell at SRC for the logistical support

To all of you for being online with us!



Reminder: Invoicing and Deliverables



Regular invoicing

Invoice on regular basis: monthly is preferred

Excess money (calendar year) is considered profit and taxable!

Spending must occur within contract period

Invoicing expected to be at or above 95% invoiced at end of each contract period

Final invoice within 60 days after project ends



Submit deliverables on time: even 1 day is too late!

System will flag delinquencies

Late deliverables will stop invoices being paid and can jeopardize future funding

Contact SRC if there are issues with getting deliverables on time



Pre-publication drafts must be deposited at SRC > 60 days before published

Best practice: deposit draft to SRC website when submitting to journal/conference (also thesis)

Update the draft on the SRC website with final paper after acceptance (select submit a new version)

Acknowledgement of SRC funding must be added to all publications



Intellectual Property Statement

<https://www.src.org/about/contracts-ip/#ip>

The information provided by researchers during this annual review

- Is the property of the university and of the researchers presenting this information
- May include research results sponsored by and provided to the funding members
- May include intellectual property rights belonging to the university and SRC, to which sponsors may have license rights

By attending or viewing this review, you are agreeing

- Not to use this information for purposes unrelated to the review unless and until approved by SRC
- To keep this information in confidence until the university and SRC have evaluated and secured any applicable intellectual property rights

After any intellectual property rights have been secured, the SRC encourages the University and researchers to publish and freely disseminate this information and results of the sponsored research program.

- Worldwide patent rights are waived if publication or public dissemination occurs prior to filing a corresponding U.S. provisional or utility patent application





General Data Protection Regulation

<https://www.src.org/app/account/guide/privacy-policy/>

- Applies to SRC
- Personal data regulations
- Involves privacy notices, consent, and security
- SRC Privacy Policy





E-Kickoff Agenda

- March 17, 2022 @ 7:00 – 8:00 pm ET (virtual)
 - 3 new tasks to e-kickoff
 - 10-min presentations + 5 min Q&A

<https://www.src.org/calendar/e007645/>

Virtual Event
All Times in ET

Tuesday, May 17, 2022		
Time	Title	Presenter
7:00 - 7:05 pm	Welcome & Introduction	Kashyap Yellai/ SRC
7:05 - 7:20 pm	Task 3113: Development of Heterobimetallic Molecular Precursors for Atomic Layer Deposition	Connie Lu / Univ. of Bonn
7:20 - 7:35 pm	Task 3114: Amorphous Metal Organic Frameworks as a Dry EUVL Resist Technology	Howard Fairbrother / John Hopkins
7:35 - 7:50 pm	Task 3115: Alternative FCC Metals for High-conductivity Narrow Interconnects	Daniel Gall / RPI



Thank You!



Kashyap Yellai

Science Director

Kashyap.yellai@src.org



SRC Research Scholars Program (Beta)

<https://www.linkedin.com/company/src-research-scholars/>

HOW TO ADD SRC RESEARCH SCHOLAR EXPERIENCE

In the Experience section, click the plus icon to add experience. (If you don't already have an experience section, you will need to [add one](#).)

- Enter **Research Scholar** as your Title.
- Select **SRC Research Scholars Program** as your Company.
- You will also need to add a Start Date for when you began working on SRC research.

Experience +

Add experience ×

Title * 2

Employment type

Country-specific employment types
[Learn more](#)

Company * 3

In the Description field, you can add the following information of your choosing:

- Thesis title
- Area of research interest
- Brief project description
- SRC Research Program and task title (e.g., SRC Hardware Security "Task Title")

Keep in mind that the description field is searchable by hiring managers so make this content count!





New SRC Student Platform on LinkedIn (Beta)

<https://www.src.org/student-center/handbook/linkedin/>

Join
the
Beta
Now!

- SRC Student Programs is rebranding to “**SRC Research Scholars**” Program
- What is the **SRC Research Scholars Program**?
 - SRC provides undergrads, graduate students, and postdoctoral researchers with a unique education consisting of traditional course work, cutting-edge research, and direct interaction with the semiconductor industry
 - These Research Scholars work on industry-relevant research with SRC-funded faculty who are recognized experts in their fields
 - Through our extensive community of academics and industry personnel, we nurture the evaluation of the talent pipeline for our industry and beyond
 - Our alumni have become industry leaders and renowned faculty researchers, creating a virtuous cycle where mojo begets mojo

SRC encourages all undergrads, graduate students, and postdoctoral researchers to join this Beta program!!!

Get LinkedIn with SRC

SRC uses a special LinkedIn Affiliate page for the SRC Research Scholars Program. Undergrad, graduate students, and postdoctoral researchers participating on SRC research add their SRC Research Scholars experience to their LinkedIn profile. This allows Scholars a way to professionally showcase their talent and experience. It also simplifies how recruiters, engineers, and even other Scholars can find SRC Research Scholars, using either the LinkedIn Search* or LinkedIn Recruiter*.

SRC Research Scholars
Program*



By being part of our community, Research Scholars will have a unique opportunity to get to know professionals with careers in the semiconductor industry or government, top researchers in their fields, and other students with similar interests.

SRC Select Disclosure



SRC Liaison Program

Maximizing the Value of Participation

Move Yourself, Your Company and the Next Generation Forward

Develop the Workforce

- Provide relevant guidance for industry challenges
- Prepare students to enter industry or pursue future academics

Contribute to Research

- Encourage technology exchange between university and industry
- Bridge the conventional gap between academia and industry

Academia Contributes to Industry

- Provide an out of the box approach to current problems which enhance industry research and development enables a differentiated product for the marketplace
- Provide an outside perspective adding diversity to the thought process of how best to attack a challenge

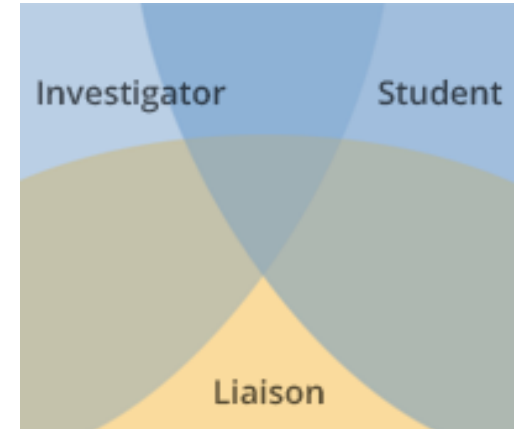
Access New Technology

- Gain valuable insights into problems and solutions that will ultimately impact industry competitiveness
- Provide an effective way to deliver actionable research results directly into their companies

Identify the Best

- Identify the most compelling research from current and recent research

Expectation to have regular PI-Liaisons calls at least one every 4-8 weeks





Effective collaboration begins with communication

SRC Program Manager

- Runs Advisory Board and aligns research
- Educates PI about requirements and responsibilities
- Encourages Liaison participation
- Finds opportunities for further engagement

University PI

- Pursues ambitious, ground-breaking research
- Schedules regular calls, every 4-8 weeks
- Arranges meet-ups at conferences
- Presents research at annual reviews

Student

- Leads meetings
- Presents findings
- Aims to present at TECHCON
- Is knowledgeable about SRC members

Liaison

- Provides industry perspective to PI
- Transfers technology into company
- Advocates for SRC research
- Coordinates with Advisory Board

Academics solving meaningful problems

Increase of tech transfer

Clear investment ROI