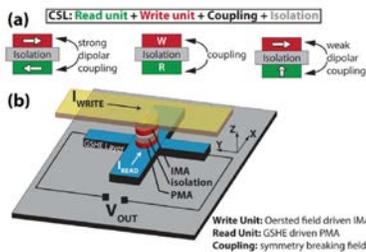
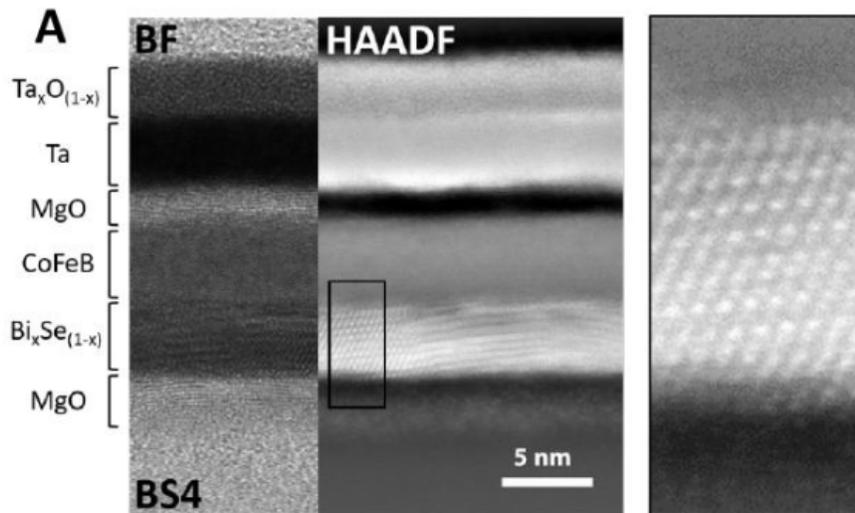
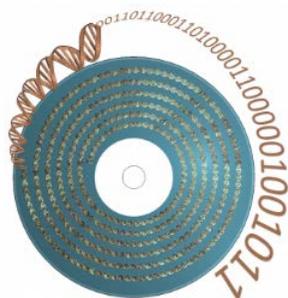


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Experimental demonstration of a spin logic device with deterministic and stochastic mode of operation [Read More](#) »

Using a device based on coupled in-plane magnetic anisotropy (IMA) and perpendicular magnetic anisotropy (PMA), nCORE researchers have demonstrated a novel spin logic system working at room temperature without the need of an external magnetic field. [Read More](#) »



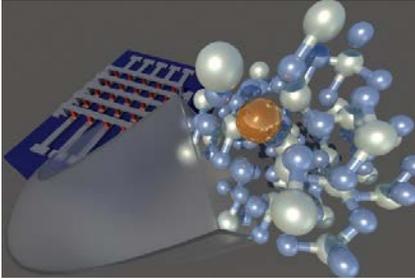
SRC SemiSynBio researchers awarded \$2M by the Higher Education Research Council (HERC) Idaho Global Entrepreneurial Mission (IGEM)

A research team, led by Will Hughes, will establish a world-class NAM institute to bring together the necessary infrastructure, resources and expertise needed to pioneer NAM technologies and to educate a future NAM workforce. [Read More](#) »



SRC and microelectronics fabrication is a key element of Notre Dame's growing research agenda

Notre Dame continues to expand its scientific research and scholarly endeavors. Programs such as the JUMP ASCENT center have resulted in a 75% increase in external research funding when compared to 10 years ago. [Read More](#) »



Silicon compatible Sn-based resistive switching memory

A team of scientists at the University of Chicago's Institute for Molecular Engineering (IME) are examining tin as a novel material to create new cost-effective, 3D memory devices. Sn diffuses fast enough to form the extended filaments required for device performance and, unlike Ag or Cu, is Si-compatible. This work was funded, in part, by STARnet's LEAST Center. [Read More](#) »



UVA CRISP Director and co-authors receive ISCA-2018 Influential Paper Award

JUMP researchers received the 2018 ACM SIGARCH and IEEE-CS TCCA ISCA Influential Paper Award for their 2003 manuscript on "temperature-aware microarchitecture." The award recognizes the paper from ISCA proceedings 15 years earlier with the most impact on the field (in terms of research, development, products or ideas) during intervening years. [Read More](#) »



SRC researcher appointed to A. Robert Noll Chair in Engineering

Vijay Narayanan, distinguished professor of computer science and engineering and electrical engineering, has been appointed to the A. Robert Noll Chair in Engineering by the Penn State College of Engineering. [Read More](#) »



STARnet SONIC work graduates with receipt of a \$61M DARPA ERI Award

3D technology developed in the STARnet SONIC program by Stanford PIs Mitra and Wong, and graduate student Shulaker (now a Professor at MIT) could give a performance boost so large that chips built at SkyWater Technology's 90-nm foundry could beat those built using today's most advanced 7-nm technology. [Read More](#) »

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