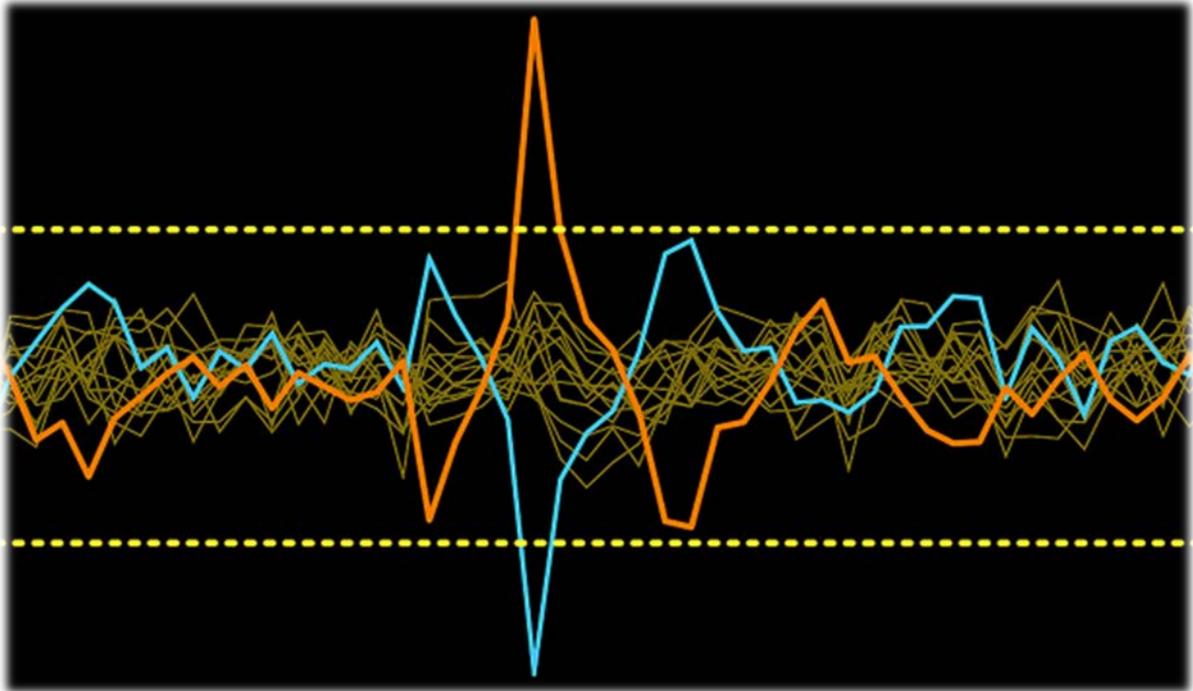


Preventing AI from divulging its own secrets. [Read more](#) »



Erik Vrieling/IEEE Spectrum



“Happy Hour with Architects” by Prof. Samira Khan

CRISP PI, Prof. Samira Khan of the University of Virginia talks about Persistent Memory with fellow CRISP Faculty Member, Prof. Steve Swanson (UCSD) and Intel’s Andy Rudoff. Check out the interesting interview between Samira, Steve, and Andy, [here](#) »



Royal Society elects ASCENT Theme Leader Prof. Ramamoorthy Ramesh

Prof. Ramesh (Physics and Materials Science & Engineering at UC/Berkeley) and Theme 2 leader of our JUMP ASCENT Center has been elected as a foreign member of the Royal Society for his “discoveries in complex oxides, from novel fundamental physics, through new materials, to their application in ferroelectric, magnetic and multiferroic devices.” The Royal Society is a Fellowship of many of the world's most eminent scientists and is the oldest scientific academy in continuous existence. Past Fellows and Foreign Members of the Royal Society have included Isaac Newton, Charles Darwin, Albert Einstein, Dorothy Hodgkin, and Stephen Hawking. [This year’s class](#) also includes Nobel Prize winner, Prof. Frances Arnold. [Read more](#) »



UT/Austin researchers receive funding to develop 3-D memory technologies

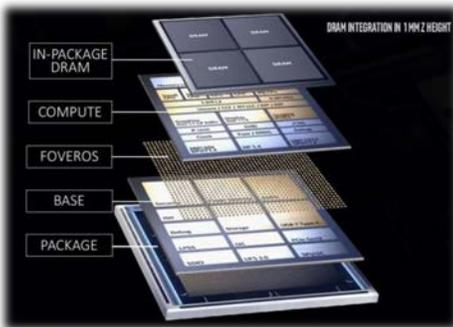
Drs. Kulkarni and Dodabalapur receive SRC funding to explore alternative device technologies to meet the ever increasing demand for high performance/energy efficient computing. Towards this goal, multiple 3-dimensional integration technologies have been proposed to continue transistor scaling beyond the traditional 2-D scaling. [Read more](#) »



ADA PI elected to 2020 class of the American Academy of Arts and Sciences

The American Academy of Arts & Sciences has elected UIUC computer architect, Prof. Sarita Adve, as one of its newest members, joining other artists, scholars, scientists, and leaders in the public, non-profit, and private sectors that are advancing the public good. The prestigious list of members includes Benjamin Franklin, Alexander Hamilton, Ralph Waldo Emerson, Charles Darwin, Albert Einstein, Winston Churchill, Milton Friedman, Martin Luther King, Jr, and Nelson Mandela. [Read more](#) »

Photo courtesy David Mercer



Wanted: process engineers versed in packaging

The new frontier of leading-edge IC design is packaging, according to ASCENT and C-BRIC PI, Prof. Arijit Raychowdhury, an expert in digital and mixed-signal design who teaches VLSI courses at Georgia Tech. Arijit recently sat down with EE Times contributor, Junko Yoshida, to talk about the challenges in teaching EE courses in the pandemic era, to transistor scaling, new fields of expertise for next-generation VLSI designers to master, what is changing in education, the disconnect between universities and industry, and what sort of engineering students are most likely to succeed in the real world in the near future. [Read more here](#) or [listen to the interview here](#) »



Prof. Anand Raghunathan receives Qualcomm Faculty Award

Prof. Anand Raghunathan, Assistant Director of the C-BRIC Center, has been chosen to receive a 2020 Qualcomm Faculty Award. The Qualcomm Faculty Award (QFA) supports key professors and their research through a \$75,000 charitable donation to their university. The goal of the QFA funding is to strengthen Qualcomm's engagement with faculty who are playing a key role in Qualcomm's recruiting of top graduate students. Prof. Raghunathan's research spans various topics in VLSI and Computer Engineering, including System-on-chip design, domain-specific architecture, computing with nanoscale post-CMOS devices, and heterogeneous parallel computing. Raghunathan is currently Chair of the VLSI area in the School of ECE. [Read more](#) »



SRC PIs offer a new "Spin" on memory

University of Arizona researchers report a discovery that opens new possibilities in the development of spintronics, a new type of memory storage capable of processing information much faster than current technology while consuming less energy [Read more](#) »



Prof. Raychowdhury selected for 2020 Qualcomm Faculty Award

Prof. Arijit Raychowdhury has been selected for the 2020 Qualcomm Faculty Award (QFA) for his contributions to low-power system-on-a-chip (SoC) design, including his group's work on power-management and clocking circuits that have impacted Qualcomm's internal research and development. [Read more](#) »

SRC opens five solicitations



We are leveraging best-in-class software, OpenWater, to manage the SRC research solicitation and selection process. Five solicitations opened June 1st for research in the areas of AI Hardware, Analog/Mixed-Signal Circuits, Systems and Devices, Logic and Memory Devices, Nanomanufacturing Materials and Processes and Environment, Safety and Health to be funded beginning January 1, 2021. [Read more](#) »

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