

Microelectronics Manufacturing USA Institute Information Webinar

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We have been here before!



Who SRC Is, Today?



Chartered to discover and accelerate technologies and create the workforce for the semiconductor industry































































SRC's Plan for the Decade

The What



January 2021
2030 Decadal Plan for
Semiconductors

The How





March 2023 Microelectronics & Advanced Packaging

Technologies (MAPT) Roadmap - Interim

Implementation





Operation of
Microelectronics
Manufacturing USA
Institute



CHIPS & Science Act of 2022 = Signed!



\$11B, 9906 investments for R&D

- National Semiconductor Technology Center (NSTC)
- National Advanced Packaging
 Manufacturing Program (NAPMP)
- Microelectronic Manufacturing USA Institute(s) (MMUSAI, MMI)
- Workforce Development

SRC is a partner with both ASIC & MITRE's Semiconductor

SRC's focus as a performer for CHIPS ACT

August 9, 2022

An Unprecedented Commitment to Semiconductor Innovation



SRC's Call to Action

- The ICT opportunities of tomorrow are simply unachievable based on current semiconductor technologies and 2D-scaling as we are up against fundamental physical limits.
- A crisis is at hand, where the semiconductor paradigm must shift to create the desired value with 3D microelectronic and advanced packaging technologies (MAPT) as the key driver.
- We must seek and create energy efficient systems via eco-considerate manufacturing.
- SRC is committed to workforce development and broadening participation. There is a bright future in semiconductors, but we must change our narratives to win over the hearts and minds of next gen innovators.

SRC will pursue a MAPT Microelectronics Manuf. USA Institute.

It will keep us at the edge of innovation, investing in early-stage ideas and tech-maturation, exploring big bets through a fast-fail and tech-transfer mindset.





