Semiconductor Research Corporation
TECHCON

CONFERENCE
AT A GLANCE
SEPTEMBER 23

SUNDAY 10TH

Time	Event
2:00 PM - 6:00 PM	CONFERENCE REGISTRATION
2:00 PM - 5:30 PM	PRESENTATION SETUP
5:30 PM - 7:30 PM	SCHOLAR DINNER AND EVENT

MONDAY 11TH

7:00 AM - 7:45 AM
BREAKFAST
Rio Grande B

8:00 AM -8:45 AM
OPENING KEYNOTE ADDRESS
Grand Ballroom

9:00 AM -10:40 AM
SESSIONS 1-5
SESSION 1: SECURITY - DESIGN
SESSION 2: MEMORY & STORAGE - ADVANCED PACKAGING AND 3D INTEGRATION
SESSION 3: ENERGY EFFICIENT - APPLICATIONS AND ALGORITHMS
SESSION 4: COMMUNICATION - ADVANCED FABRICATION TECHNIQUES
SESSION 5: SMART SENSING - DEVICES

9:00 AM - 9:40 AM
WELCOME & UNDERGRADUATE INNOVATION SESSION 1
Trinity B

10:40 AM -11:00 AM
BREAK
Plaza Foyer

11:00 AM - 12:40 PM
SESSIONS 6-11
SESSION 6: SECURITY - TOOLS AND VALIDATION
SESSION 7: MEMORY & STORAGE - SYSTEM ARCHITECTURES
SESSION 8: ENERGY EFFICIENT - ADVANCED PACKAGING
SESSION 9: COMMUNICATION - BACKSCATTERING, MIMO AND HIGH SPEED COMPONENTS
SESSION 10: SMART SENSING - CALIBRATION AND MEASUREMENTS
SESSION 11: SUSTAINABILITY - PER- AND POLYFLUOROALKYL SUBSTANCES (PFAS)

12:40 PM -1:40 PM
LUNCH
Rio Grande B

1:40 PM - 3:20 PM
SESSIONS 12-16
SESSION 12: SECURITY - ADVANCED PACKAGING AND DEVICES
SESSION 13: MEMORY & STORAGE - DESIGN FOR HIGH PERFORMANCE COMPUTING
SESSION 14: ENERGY EFFICIENT - SYSTEM ARCHITECTURES
SESSION 15: COMMUNICATION - GaN AND III-V DEVICES
SESSION 16: SMART SENSING - TOOLS, VERIFICATION, AND VALIDATION

1:40 PM - 2:20 PM
UNDERGRADUATE INNOVATION SESSION 2
Trinity B

3:20 PM - 5:15 PM
TECHCONNECT
Rio Grande A

5:15 PM - 6:15 PM
RECEPTION
Grand Ballroom Foyer

6:15 PM - 7:45 PM
CONFERENCE BANQUET & PRESENTATION OF AWARDS
Grand Ballroom

TUESDAY 12TH

7:00 AM - 7:45 AM
BREAKFAST
Rio Grande B

8:00 AM -8:45 AM
SPECIAL PANEL SESSION
Grand Ballroom

9:00 AM -10:40 AM
SESSIONS 17-21
SESSION 17: SECURITY - VERIFICATION
SESSION 18: MEMORY & STORAGE - MATERIALS, PROCESSES, AND DEVICES
SESSION 19: ENERGY EFFICIENT - DESIGN
SESSION 20: COMMUNICATION - FRONT END CIRCUITS AND ANTENNAS
SESSION 21: SMART SENSING - SYSTEM ARCHITECTURES

9:00 AM - 9:40 AM
UNDERGRADUATE INNOVATION SESSION 3
Trinity B

10:40 AM -11:00 AM
BREAK
Plaza Foyer

11:00 AM - 12:40 PM
SESSIONS 22-27
SESSION 22: SECURITY - SYSTEM ARCHITECTURES
SESSION 23: MEMORY & STORAGE - AI/ML APPLICATIONS
SESSION 24: ENERGY EFFICIENT - MATERIALS AND DEVICES
SESSION 25: COMMUNICATION - ADVANCED PACKAGING AND HETEROGENOUS INTEGRATION
SESSION 26: SMART SENSING - DESIGN
SESSION 27: SUSTAINABILITY - GREEN PROCESSES

12:40 PM -1:40 PM
LUNCH
Rio Grande B

1:40 PM - 3:20 PM
SESSIONS 28-32
SESSION 28: SECURITY - APPLICATIONS AND ALGORITHMS
SESSION 29: MEMORY & STORAGE - COMPUTE IN MEMORY
SESSION 30: ENERGY EFFICIENT - TOOLS, VERIFICATION, AND VALIDATION
SESSION 31: COMMUNICATION - BEAMFORMING CIRCUITS AND ARCHITECTURES
SESSION 32: SMART SENSING - APPLICATIONS AND ALGORITHMS

1:40 PM - 2:20 PM
UNDERGRADUATE INNOVATION SESSION 4
Trinity B

3:20 PM - 5:15 PM
TECHCONNECT
Rio Grande A

5:15 PM - 7:15 PM
CAREERCONNECTIONS
Grand Ballroom Foyer

7:30 pm
SCHOLAR BANQUET & AWARDS
Grand Ballroom

Smart Sensing: The Analog Data Deluge	**Memory & Storage:** The Growth of Memory and Storage Demands	**Communication:** Communication Capacity vs. Data Generation	**Security:** ICT Security Challenges	**Energy Efficient:** Compute Energy vs. Global Energy Production	**Sustainability**